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Manual by: Owen P Walker Copyright © 2001

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# TABLE OF CONTENTS

IMPORTANT NOTICE 2
CERTIFICATES & COMPLIANCES
SAFETY SYMBOLS
MAINS PLUGS & MAINS POWER CORDS7
OBTAINING TECHNICAL SUPPORT
Section 1 - Introduction9
1.1 Product Description9
1.2 Manual Summary9
1.3 System Overview10
1.4 Unpacking 11
1.5 Equipment Supplied11
1.6 Static Precautions12
1.7 Environment 12
SECTION 2 - ENGINE PHYSICAL INSTALLATION 13
2.1 Mechanical Installation13
2.2 Electrical Installation14
2.3 Engine Rear Panel Connections 15
2.4 Cabling16
<ul> <li>2.5 External Cable Length Considerations 17</li> <li>2.5.1 MFX3.48 Control Cable</li></ul>
2.6 Connecting The Various System
Section 3 Physical Installation - Mfx
3.1 Introduction19
3.2 Mechanical Installation19
3.3 Electrical Installation
3.4 Interconnecting The Console And
3.6 Internal and External SCSI Devices 21

SECTION 4 - SYSTEM CONFIGURATION
4.1 Introduction 22
4.2 Switching on the Equipment
4.2.1 Console 22
4.2.2 Engine 22
4.3 Boot up screen 24
4.3 Software Installation
4.6.1 Installing Software From Exabyte 26
SECTION 5 - CONFIGURING DISK DRIVES
5.1 External Hard Disk Drives
5.1.1 Wide Based Disk Drives
5.1.2 Narrow Based Disk Drives 27
5.1.2 Configuring The Engine For Narrow Use
5.1.3 Mixing Wide And Narrow Disk Drives
5.2 Checking for Newly Installed SCSI Devices
5.3 Setting Up Removable Media
SECTION 6 - ROUTINE MAINTENANCE
6.1 Cleaning The Exterior Of The Units 31
6.2 Cleaning The Monitor Screens
6.3 Cleaning The Engine Air Filter
New Air Filters 32
APPENDIX - CONNECTION SPECIFICATIONS
Introduction
A1 - Audio I/O Configuration
QDC Configuration
A1.1 Analog Audio I/O Specifications 35
A1.2 Digital Audio I/O Specifications 35
Appendix - Connection & Signal
A2 Control & Reference Pinouts 50
A3 General Purpose Interface
A4 Mixer 52



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A5 Controller 53
A6 Printer 54
A8 9-Pin A 55
A9 9-Pin B 55
A10 9-Pin Slave 56
A11 LTC - I/O
A12 Serial Port 57
A13 Midi I/O
A14 AES Sync I/O 58
A15 Modem 58
A16 VGA 59
A17 Video Sync - In 59
A18 Word Clock - In 59
A19 Word Clock - Out 59
A20 Dimensions 60
A20.1 Engine 60
A20.1 Power Requirments 61
A20.2 MFX3.48 Console61

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# **CERTIFICATES & COMPLIANCES**

## CC EMISSION CONTROL

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications.

The MFX3.48 Console and Engine conform to the EMC directives :-

EN 55022:1998 Class A - Emissions

EN 55024:1998 Class 3 - Susceptibility

The MFX3.48 Console and Engine also conform to the Safety directives :-

UL 1419 - Professional Video & Audio Equipment

IEC 60950 - Information Technology Equipment

For further information on EMC procedures please refer to the following titles:-

Noise Reduction Techniques In Electronic Systems by Henry W Ott

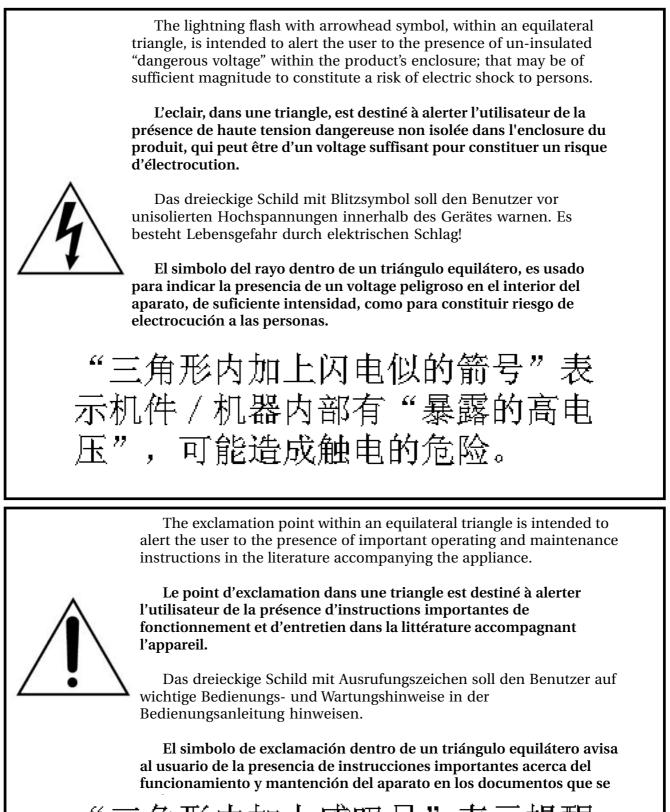
EMC by Tim Williams

Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense. Changes or modifications not expressly approved by Fairlight ESP can affect emission compliance and could void the user's authority to operate this equipment.

CHARACTERISTICS	DESCRIPTION		
Equipment Type	Supplemented Data: Information Technology		
Equipment Class	Supplmental Data: Class 1 - Grounded equipment		
Installation Catergory	Requirment Category 2 - Local level appliances, portable equipment etc.		
Pollution Degree	Requirment: Level 2 operating enviorment - Normally only non-conductive pollution occurs. Occasionally there may be a temporary conductivity caused by condensation.		



# SAFETY SYMBOLS



"三角形内加上感叹号"表示提醒 使用者查阅附上的"重要操作和维 修指南"。

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# MAINS PLUGS & MAINS POWER CORDS

The following lists the recommended Mains Plugs and Leads for use in various countries throughout the world.

Mains Attachment Plugs				
Standards applicable for Mains Plugs	Rating	Country		
ASTA BS1363 1984	10A @ 250VAC	UK		
BS546, 1950	10A @ 250VAC	India, Kenya, Nigeria, Kuwait, Parts of Asia and the Far East		
IEC695-2-1 & NF-USE	10A @ 250VAC	France & Belgium		
DIN49441 & CEE 7 Sheet VII	10A @ 250VAC	Europe		
SEV	10A @ 250VAC	Switzerland		
CEI23-16	10A @ 250VAC	Italy		
NEMA5-15P & NEMA6-15P	10A @ 250VAC	USA		
Mains Po Standards applicable for Mains Leads	ower Leads Rating	Country		
CSA22.2 No.42 & UL498	10A @ 250VAC	Canada & Japan		
ASE 1011 (1959)	10A @ 250VAC	Switzerland		
CEI 2316	10A @ 250VAC	Italy		
SRAF 1962	10A @ 250VAC	Denmark		
AS3112-1990, NZSS198-1967	10A @ 250VAC	Australia, New Zealand, Fiji, Papua New Guinea, Republic of China		
UL498 & SJT 10A minimum rating with IEC60320- 1 coupler	10A @ 250VAC	USA		



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# **OBTAINING TECHNICAL SUPPORT**

Users requiring technical support should contact their local Fairlight office or distributor.

Information can also be found on the world wide web at :-

http://www.fairlightesp.com

United Kingdom	USA – West Coast		
Fairlight ESP Limited Unit 12, Spectrum House 32-34 Gordon House Road London NW5 1LP England Tel: +44 171 267 3323 Fax: +44 171 267 0919	Fairlight USA 844 North Seward Street, Hollywood, CA90038 USA Tel: +1 323 465 0070 Fax: +1 323 465 0080		
France	USA – East Coast		
Fairlight France 41-43 Rue des Peupliers 92100 Boulogne-Billancourt Paris France Tel: +33 1 4610 9292 Fax: +33 1 4610 9295	Fairlight USA 2 West 45 <sup>th</sup> Street, Suite 605 New York, NY 10036 USA Tel: +1 212 819 1289 Fax: +1 212 819 0376		
Germany	Japan		
Fairlight Deutschland Gmbh August-Bebel-Strasse 26-53 14482 Potsdam-Babelsberg Berlin Germany Tel: +49 331 721 2930 Fax: +49 331 721 2933	Fairlight Japan Inc. Seijyo 4-2-23, Setagaya-Ku, Tokoyo 157-0066 Japan Tel: +81 3 5940 1515 Fax: +81 3 5940 1516		
Asia - Pacific			
Fairlight ESP Pty.limited Unit 2, 1 Skyline Place, Frenchs Forest NSW 2086 Australia Tel: +61 2 8977 9999 Fax: +61 2 8977 9900			

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# SECTION 1 - INTRODUCTION

#### **1.1 PRODUCT DESCRIPTION**

MFX3.48 marries together the MFX user interface with the technology of the latest generation of digital audio workstation in a combination that has a significant impact on productivity and efficiency.

The MFX3.48 interface incorporates a dedicated editing Console, a unique graphical user interface and powerful database capabilities. These tools allow the operator to work quickly and efficiently without impeding the creativity of the recording and editing process, and can now be fully utilized in the professional audio suite. The MFX3.48 offers a number of unique benefits through its adaptation of DSP and proprietary technology to the audio environment.

For a more detailed explanation of the MFX3.48, please refer to the MFX3.48 User Manual.

#### **1.2 MANUAL SUMMARY**

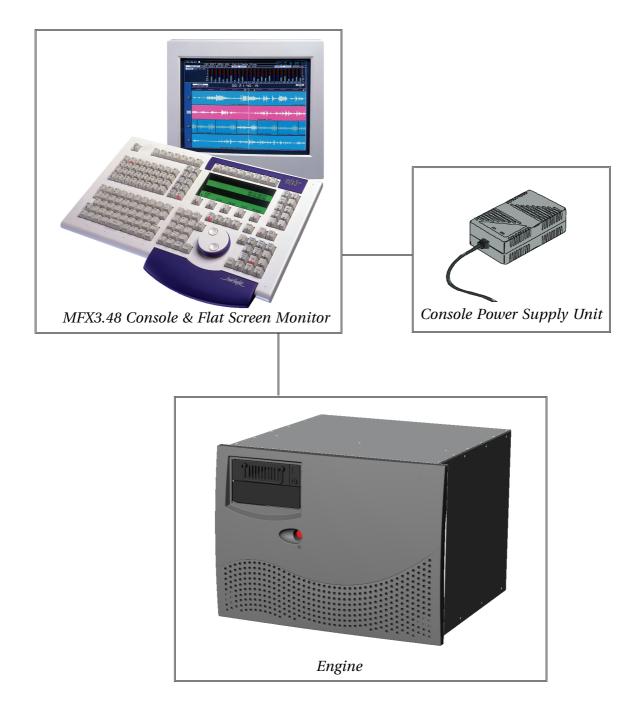
The purpose of this manual is to provide all the technical information required to design a suitable installation for the MFX3.48 system and to fit and connect the units. It is assumed that the installer is familiar with both analogue and digital audio signals. For full operational instructions please refer to the MFX3.48 *User Manual*.



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## **1.3 System Overview**

The MFX3.48 is made up of four interconnecting components, the MFX3.48 Console and external power supply and a Engine rack mounted unit which contains all the electronic circuitry, disk drives and audio I/O necessary to run the system.



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## **1.4 UNPACKING**

It is advisable that before any installation work is attempted that the system be unpacked and the contents verified. A sizeable area approximately 3 meters square should be suitable. Using the basic packing list attached record the items you have received. This will both help you when you have to make an enquiry, by having the relevant details logged in your Installation Manual, in the event of a packing omission. At this stage do not power up the system, nor remove electronic modules from the system, as damage may occur if not handled correctly.

EQUIPMENT SUPPLIED	QUANTITY	CHECK LIST
Engine	1	
Console	1	
Console External Power Supply Unit	1	
Mouse	1	
MFX3.48 Installation Manual	1	
MFX3.48 User Manual	1	
MFX3.48 Console Controller Cable	1	
Keyboard	1	

## **1.5 EQUIPMENT SUPPLIED**

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#### **1.6 STATIC PRECAUTIONS**

Please take note that all Fairlight manufactured electronic modules are static sensitive and should be handled under anti static conditions. When working on a system always ensure that you have an anti static lead connected and that the system is connected to ground through an earth lead.

Never work on the system while powered up unless you are authorized by Fairlight to do so. As a matter of practice always touch the external chassis of the system before opening the front panel. If cards are not handled under anti static procedures your machine may sustain damage which could either cause a complete failure or may cause intermittent crashes and subsequential system failure.

When handling cards please ensure that they are placed in anti static bags when not in the system. For shipment purposes electronic modules should be placed in an anti static bag and then suitably surrounded with loose packaging materials in a solid card board box. Cards shipped to Fairlight without the correct anti static packaging will have their warranty voided. If you have any enquiries on this matter please feel free to contact your local Fairlight office or distributor.

#### **1.7 Environment**

The system is designed to be operated in a clean air-conditioned environment. Generally, an area comfortable for people ( $20^{\circ}$ C -  $21^{\circ}$ C) should be suitable. The rack mounted units and disk drives, use fans for ventilation. Users may find it desirable to install these units away from the operator/console location. Note that cable lengths, as detailed in *Section 2.5 - External Cable Length Considerations*, should be taken into account when planning the installation.

Make sure that the rack units can access cool air through the opening on the front of the Engine and expel warm air from the back. As with all computer systems, the Fairlight will operate more reliably if static generating floor coverings are avoided.

Do not fit the Engine into a closed environment except where ducted cool air is forced through the Rack. Do not run the Engine whilst it is on the ground as it will accumulate dust, which may eventually cause a failure.

The Engine unit is normally installed in a suitable 19" rack which is at least 600 mm deep, or has an open back section. It is recommended that external hard drives be mounted on a rack tray, above or below the Engine, using the shortest possible cables to prevent SCSI bus corruption.

Please Avoid:

- Fitting the Engine where air circulation will be restricted.
- Installing Engine close to heat sources.
- Installing in dusty or damp area.
- Installing in area subject to vibration.
- Installing in area with strong magnetic or electric fields

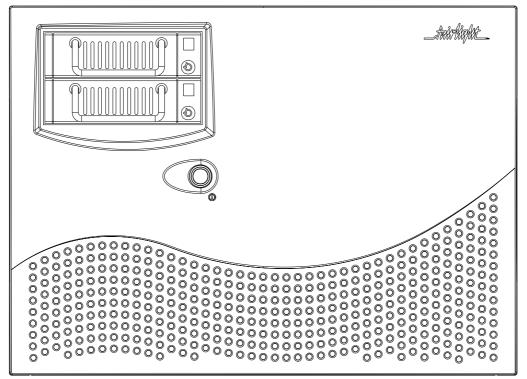
# SECTION 2 - ENGINE PHYSICAL INSTALLATION

## **2.1 MECHANICAL INSTALLATION**

The Engine unit, takes up 8 RU, when fitted into a 19" rack. It operates from either 100-120v or 200-250v, 50-60Hz with the mains inputs being auto- switching, as such there are no switches to be set. At least two people are required to fit the Engine into a 19" rack as the unit is quite heavy. If available it is suggested that a third person be made available for the initial installation into the rack, such that a person can guide the Engine into the rack, from the rear.

The Engine should be fitted so that there is no restrictions to the ventilation, at the rear of the unit. If external SCSI devices are to be connected it is recommended that these be placed on a rack tray above or below the Engine.

It should be noted that typically the Engine unit is fitted with a boot drive with SCSI I.D. " 0 "( although the boot drive can be at any ID ). The Engine uses fans for its forced ventilation system, these generate an amount of ambient noise. As stated before the Engine should be located in an air conditioned machine room away from the studio and other heat generating equipment.

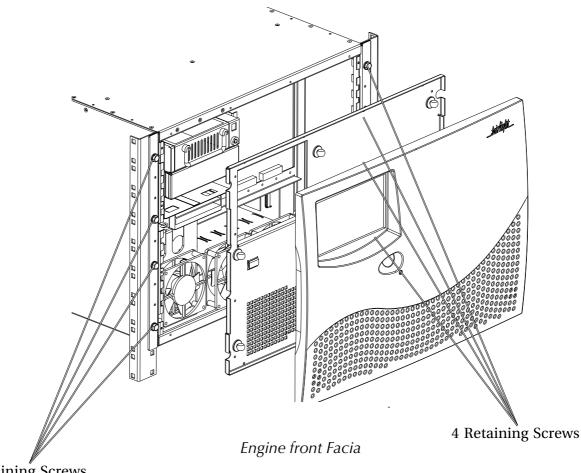


Engine Front Panel

1 Install the Engine into a 19" Rack unit. Once this is done, the front facia of the Engine must be removed to allow 8 retaining screws to be fitted.

2 To mount the Engine into the rack, remove the front dress panel and insert 4 retaining screws on to both the left and right rack mounts. Once this is done reattach the front dress panel.





4 Retaining Screws

#### **2.2 ELECTRICAL INSTALLATION**

The Engine is designed to run from a single phase power source with one of its current carrying conductors at or near ground earth ground potential ( the neutral conductor). Only the line (live) conductor is fused for over-current protection. Systems that have both current carrying conductors live with respect to ground , such as phase-to-phase in multi-phase systems are not recommended as power sources.

It is recommended that both the Engine and Console are connected to the same electrical supply or ring main which ideally should be regulated and smoothed. If the power source is likely to be unexpectedly cut then both the units should be fed from a backed up power source such as a UPS.

For Power Requirements, see Appendix A21 at the back of this Manual.

Mains connections should be fitted with the appropriate type of plug. See Section *"Mains Plugs & Power Cords"* at the front of this Manual.

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## 2.3 ENGINE REAR PANEL CONNECTIONS

The Engine rear is split into two distinct sections; the System I/O Panel, located to the right and the Audio I/O section, located to the left.

The System I/O Panel contains all the synchronization needed to allow communication with external devices including RS-422, Midi, LTC, AES Sync, Serial and GPIO.

To allow communication with other Fairlight products including Fame and Prodigy, a Mixer port is provided.

To allow comunication with the MFX3.48 Console, a Controller port is provided.

The System I/O Panel also contains the VGA Output, Word Clock and Video sync In connectors.

The Audio I/O Section can contain up to a maximum of 4 QDC cards, which perform all analog ue and digital I/O Connections and Processing.

Each QDC card can contain a maximum of -

• 16 analog inputs and 32 analog outputs

or a maximum of -

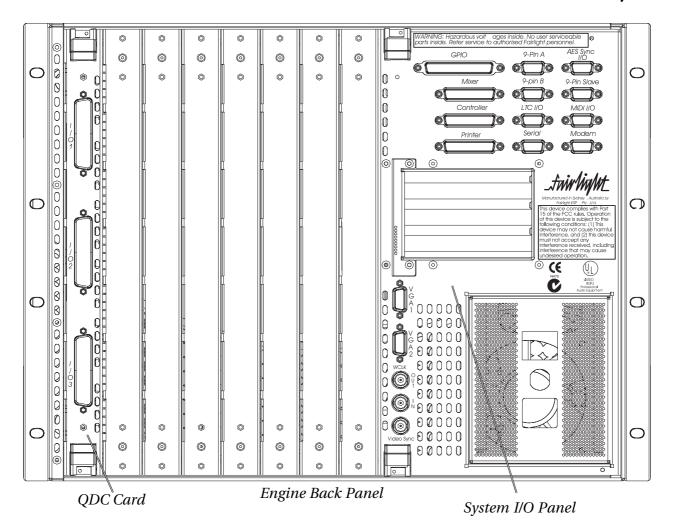
- 32 digital inputs and outputs
- 16 analog inputs.

Depending on what configuration is ordered, the system can allow for more QDC cards to be configured. Please contact your local Fairlight office or distributor for further information regarding the maximum inputs and outputs which can be configured within the system.

Please refer to the Appendix page 37 for further details on the Audio I/O and System I/O Panels.



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## 2.4 CABLING

Once the mechanical installation of the Engine is complete, a number of cables must be connected. Some of these connect various components together and may have been supplied with the MFX3.48 system, others provide connection to the rest of the environment.

The interconnecting cables suppled as standard may not be long enough for your installation, in which case you must provide longer cables. The following information gives specific wiring details and highlights any special requirements, however as with all equipment not supplied with the MFX3.48 system, it is the installer's responsibility to ensure that these cables comply fully with the applicable safety and EMC regulations.

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## 2.5 EXTERNAL CABLE LENGTH CONSIDERATIONS

Poor cabling can be the bane of a good system. Earth loops, floating inputs and outputs and extended runs are just some of the issues to be addressed when planning an installation.

#### 2.5.1 MFX3.48 CONTROL CABLE

The MFX3.48 Control Cable connects the MFX3.48 Console to the Engine. The cable carries RS232, RS422 and MIDI signals and is limited to a maximum length of 30 meters, providing correct attention is paid to signal pairing using twisted pair cable. Ensure sufficient slack is left at the Console end to ensure movement of the Console does not cause undue stress on the connector, or cause the connector to partially disconnect, which can damage the serial drivers. When fitting or reconnecting the cable ensure that power is off at either the Engine or the Console.

#### 2.5.2 SONY 9-PIN CABLE

The 9-Pin cable connects the Engine to a Sony 9-pin protocol machine. The recommended maximum length for this cable is 30 meters. The Engine is supplied with a 5 meter cable. Again it is recommended that the power be switched off at one end when connecting to protect the drivers.

#### 2.5.3 VGA CABLE

The Video cable connects the VGA output from the Engine to the attached Monitor. It is a 15-way shielded 50 Ohm cable with high density D-type connectors at each end. A maximum length of 20 meters is typical. The Fairlight Engine is supplied with a 10 meter cable which has been found to be suitable for the majority of monitors.

#### 2.5.4 AUDIO CABLE

Standard shielded audio cable is suitable.

#### 2.5.6 DIGITAL AUDIO CABLES

Digital cables connected to the Engine system must be of the correct 110 Ohm impedence.

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## 2.6 CONNECTING THE VARIOUS SYSTEM COMPONENTS

- 1 Once cabling issues have been dealt with the various system connections need to be made.
- 2 Connect an IEC Power lead to the back of the Engine.
- 3 Connect the 15 way D connector of the Video cable to VGA 1 connector on the System I/O Panel on the rear of the Engine.
- 4 Connect the Console Controller cable to the connector on the System I/O panel located on the rear of the Engine.
- 5 Connect any external SCSI drives to the external SCSI card (if fitted) and ensure that the last device is terminated. Ensure that the SCSI I.D.'s are not in conflict with each other.
- 6 Connect all Synchronization cables such as LTC, Word clock, Video Black burst etc.
- 7 Connect all audio input and output cables to the QDC cards located on the rear of the Engine.

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# SECTION 3 PHYSICAL INSTALLATION - MFX CONSOLE

## 3.1 INTRODUCTION

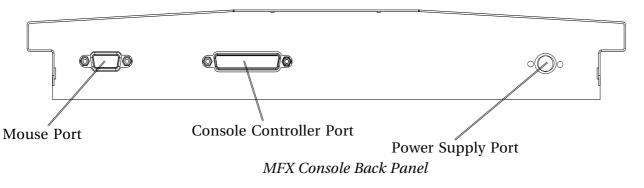
The MFX Console provides a control surface to allow the engineer to work quickly and efficiently. The Console has a number of user definable macros and keystrokes. For more information regarding the operational use of the console please refer to the *MFX3.48 User Manual*.



MFX Console

### 3.2 MECHANICAL INSTALLATION

The MFX Console should be sat on a level horizontal surface.



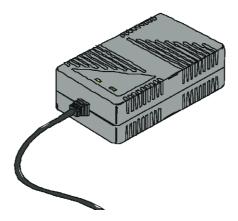


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## **3.3 Electrical Installation**

The Power Supply unit is an auto ranging unit which will take any input voltage from 100 to 250VAC using a standard IEC mains cable. As there are no mains switches on the power supply it should be plugged into a switched mains outlet. The output from the PSU is 12DV, fed to the console via an 8 pin mini DIN connector. As the output lead is fairly short, the PSU should be located close to the close.

A small LED indicator illuminates when the supply is on.



CAUTION : 1. DO NOT USE ANY OTHER POWER SUPPLY TO POWER THE CONSOLE. 2. DO NOT CONNECT THE POWER SUPPLY TO THE CONSOLE WITH THE MAINS POWER TURNED ON.

#### 3.3.1 MFX3.48 CONTROL CABLE

The MFX3.48 Control Cable connects the MFX3.48 Console to the Engine unit. The cable carries RS232, RS422 and MIDI signals and is limited to a maximum length of 30 meters, providing correct attention is paid to signal pairing using twisted pair cable. Ensure sufficient slack is left at the Console end to ensure movement of the Console, does not cause undue stress on the connector, or cause the connector to partially disconnect, which can damage the serial drivers. When fitting or reconnecting the cable ensure that power is off at either the Engine or the Console.

### 3.4 INTERCONNECTING THE CONSOLE AND ENGINE

- 1 Place the Console at a suitable location close to the mixing desk.
- 2 Connect the Console Controller cable to the 37 Pin D connector on the rear of the Console.
- 3 Connect the mouse to the 9 Pin D connector on the rear of the Console.
- 4 Connect the Console power supply cable to the MFX3.48 Console and then connect the mains power to the MFX3.48 Console power supply unit.
- 5 Connect the Keyboard cable to the Keyboard socket on the back of the MFX3.48 Console.
- 6 Once all connections have been made to the Console, it can be powered up safely, by pressing the switch located on the back panel.



## 3.5 INTERNAL AND EXTERNAL SCSI DEVICES

The Engine unit has been designed to accept two 3.5 Inch drives and 2 5.5" drive bays internally which can take either Kingston Technology drive enclosures or an Exabyte enclosure. The system typically comes configured with one system disk which contains the O/S9 Operating System and MFX3.48 software. This allows for 3 media drives to be fitted internally in the system.

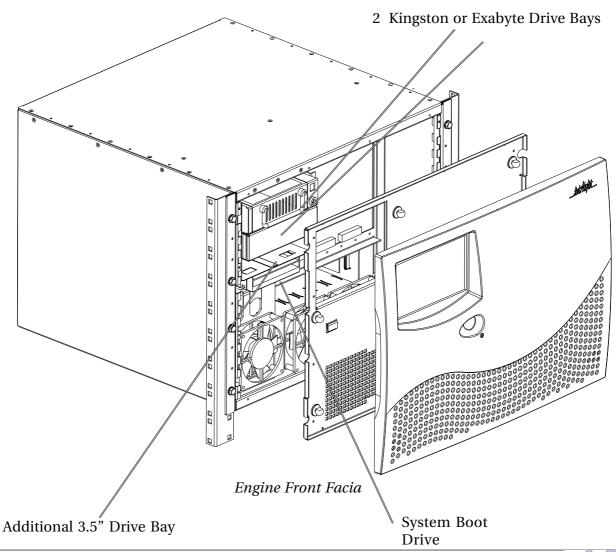
With a second SCSI Controller installed within the Engine an additional 7 SCSI devices can be attached externally.

Typically Exabyte drives should be set to ID "5" when connected on either SCSI Buss.

Optical based devices should be set to ID "3". When connected on either SCSI bus.

It is recommended that a rack tray be fitted either above or below the Engine to hold external SCSI devices. The last device on the chain should be terminated with all other devices being looped through. The cables should be the shortest length possible to avoid SCSI bus corruption.

See Section 5 - Configuring Disk Drives





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# SECTION 4 - SYSTEM CONFIGURATION

#### 4.1 INTRODUCTION

The following describes the procedure for powering on the Console and Engine.

CAUTION : NEVER ATTEMPT TO ATTACH SCSI BASED DISK DRIVES OR OTHER CABLING WHILE THE SYSTEM IS RUNNING AS THIS MAY CAUSE FILE CORRUPTION. ALWAYS POWER OFF BEFORE CONFIGURING ANY CABLES.

#### 4.2 Switching on the Equipment

#### 4.2.1 CONSOLE

Once the mouse and cables have been connected, the MFX3.48 Console can be powered up.

1. Turn on the power switch, at the power source, and on the back of the Console. The Console will then boot up.

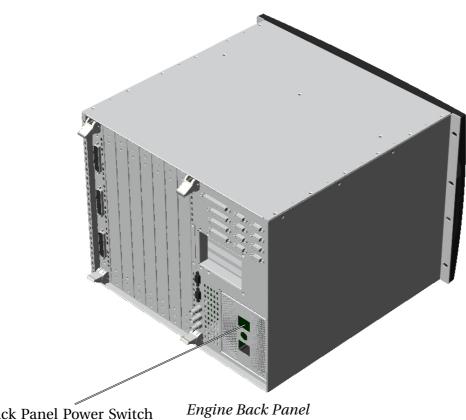
2. The green LC display on the Console should display zero's on the top screen and on the lower screen the software revision.

#### 4.2.2 ENGINE

- 1. Ensure all SCSI cables and terminators are connected.
- 2. Power up all SCSI devices.
- 3. Verify monitor cable is connected and then power up the monitor.
- 4. Ensure all Sync Input/Output cables are connected and secure.
- 5. Turn down the master faders on the mixing console.
- 6. Ensure all digital and analog Input / Output cables are connected and secure.
- 7. Ensure MFX3.48 Console cable is connected and secure.
- 8. Ensure 9 pin control cable is connected and secure.
- 9. Power up the Engine via the switch on the back panel then press the switch on the front panel.
- 10. Power up the MFX3.48 Console, if not already powered up.

At this point the system should be booting up if all has gone to plan. Initially you will observe a gray and blue text screen, containing system configuration information. The system will continue booting until the program is loaded.

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Back Panel Power Switch

Engine Front Panel

Front Panel Power Switch

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#### 4.3 BOOT UP SCREEN

At this point the system should be booting up and the following text should be seen on the graphical interface.

1. Take note of the SCSI devices being found in case a problem is encountered.

+----[ Fairlight ESP - MFX Mainframe - Flashware v1.00 [16.1.07b] ]---+ Backplane: Yes (9) Compile Date: Feb 21 2000| SIO [1]: No SIO [2]: No Compile Time: 12:05:16 IntGlue Xilinx: 34 Colour Graphics Card [A]: No TSB Xilinx: 7 Colour Graphics Card [B]: No Machine ID: 061C207A --[ Non Volatile RAM Options ]------SCSI Controller Boot Slot: 0 ROM Debuq Level: OFF SCSI Initiator ID: 7 IOPACK Mode: 46 Lines Synchronous SCSI: Yes Disconnecting SCSI: Yes Wide SCSI: Yes -----[ DIP Switch Settings ]-----Boot ROM Select (0) #B1: No Boot ROM Select (1) #B2: No Enable System Debugger #A1: No Disable PCI BIOS #A2: No HIRES CG5 Enable #B3: No Disable MMU #A3: No Disable IOPACK Output #A4: Yes DipSwitch #B4: No Change CCB to CWT #A5: No DipSwitch #B5: No QDC Disable #A6: No DipSwitch #B6: No QDC Order left->right #A7: No DipSwitch #B7: No DipSwitch #A8: No DipSwitch #B8: No PCI Buss Status ]---------[ On Board SCSI: Installed Symbios 53c875 [1000:000F:#91] Slot #1: Vacant Slot #2: Vacant Slot #3: Installed Intel i82557 [8086:1229:#90] OS-9/68040 System Bootstrap [Dynamic RAM Version] Press Key to Display Boot Menu ... 4 3 2 1 Autobooting Scanning On Board PCI SCSI Controller ID -DEVICE TYPE VENDOR PRODUCT FIRM CAPACITY SECT MB \_\_\_\_ 01 -Fixed Disk IBM DRUSAQU 0270 011191F9 512 8754 Attempting to Boot to Target #1 on the Onboard Controller Detected an RBF Formatted Disk. A valid OS-9 bootfile was found. -nt Setting Lines Per Page to 48 - [TERM=xterm] ..... Loading Resident Modules ..... Starting Resident Fairlight Extension Modules ..... flight: v3.09 - Fairlight OS9 Extensions ESP Messages Version 1.10 Initialising MMU ..... Starting RAM Resident IOPACK ..... Saving Last TVT Image (/dd/tvtlog.txt) ..... Starting Network ..... Creating primary (MASTER) network interface "/ie0" for [Web] HOSTNAME =  $mfx_232$ IP ADDRESS = 192.100.105.232 NET MASK = 255.255.0.0 DOMAINNAME = MFX INNET=FFFF0000 BDCST=FFFFFFF Creating device descriptor "ie0" "if82557" "ifman" Creating socket: Devices "/lo0 /ie0" for host "mfx\_232" Starting Network Starting TVT Drivers (TCP/IP Port 2700) .....

2. If the MFX3.48 system has been configured to be part of a network, check that the Engine is attaching to the network. For further details on connecting to an Local Area Network, please the network administrator.



Starting MFX Console ..... \_ System Identification Machine **#:061c0283** Site:Fairlight ESP Pty Ltd MERLIN 9-Pin:YES MEX Printing:YES Time-FX:YES Audiobase:TWO Multi-Out:YES A-Out:48 D-In-Out:48 A-In:48 Tracks:48 Preview:YES Connect:YES MediaLink:YES Varispeed:YES CD-Write:YES \_\_\_\_\_ Creating Temporary Directory (/dd/TMP) ..... Starting File Gopher, DFN Server, FFS, ANETD and NODED ..... Starting SCSI Caching Sub-System and Media Daemon ..... Initialising Audio Hardware ..... jtag v2.06: qdcinit v3.06 Resetting all QDCs QDC 1 Slot 7 OK DSP Resources 1 ODC cards 4 DCC sections 8 DSP processors I/O Resources: 2 AIN cards AOUT cards 2 2 DIO cards 16 Analog Inputs 16 Analog Outputs 16 Digital Inputs 16 Digital Outputs Checking Versions of Modules, ROMS and Hardware ..... Starting Session ..... 1 devices online OS-9/68K V3.0.3 MFX/PX-DYNAMIC '/term' online 00/25/06 at 19:04:31 User name?: mfx Process #32 logged on 02/01/06 19:04:52 Welcome! MFX:

3. As the boot up process continues check that the MFX3.48 Console is being seen.

4. If a problem is encounter with audio I/O check that the system is logging the QDC card or cards within the system.

5. If problems persist please contact a local Fairlight office or distributor.



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### 4.3 SOFTWARE INSTALLATION

From time to time software will need to be installed onto the MFX3.48 system. The following describes the steps which need to be taken to install software.

#### 4.6.1 INSTALLING SOFTWARE FROM EXABYTE

To update software from the Exabyte whe no release files are available.

- 1. Ensure that an Exabyte drive is connected and set to ID 5.
- 2. Power up the Exabyte drive.
- 3. Place the Exabyte software tape in the drive.
- 4. Power up the Engine.

5. Press the < Space Bar > immediately on powering up the system. You may need to press the < Space Bar > a few times. The aim is to stop the system booting from a hard drive if one is connected.

6. The displayed prompt is " Select a boot method from above menu: ".

7. Type " ROM " < Return > to boot to from Rom.

8. Type " Upgrade -T " < Return>. This tells the system to boot from the Exabyte at ID 5.

Follow the procedure as appears on the screen. You will be asked if you want to reformat the drive. Answer NO to this if there are projects you require on the drive.

# SECTION 5 - CONFIGURING DISK DRIVES

## 5.1 EXTERNAL HARD DISK DRIVES

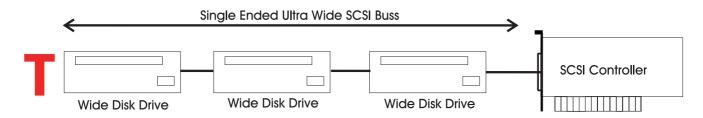
An additional SCSI controller can be added to the system and fitted in one of three PCI slots on the PXY Main Processor Board. With the controller physically installed an additional 7 SCSI devices can be connected to the system.

#### 5.1.1 WIDE BASED DISK DRIVES

The Ultra Wide single ended SCSI specification is used within the internal and external SCSI busses. This allows for ultra fast transfers speeds from a disk drive up to 40 megabytes a second.

The SCSI controller comes configured with a single ended 68 Pin configuration connector.

Only a Fairlight supplied SCSI Controller can be installed and configured with the Engine.



All cabling to SCSI devices must be kept as short as possible. The maximum length for the entire buss must not exceed more than 1.5 meters. It is highly recommended that drive enclosures with an active backplane be used to reduce cable lengths.

Some disks have SCSI terminating resistors in place and these should be checked and resistors removed when extra disks are being added to the system.

The last disk drive at the end of the chain must be terminated using an active terminator.Lower transfer rates and SCSI errors will result if the buss is not correctly terminated.

SCSI I.D.'s can be 0 through to 6. SCSI I.D. 7 is reserved for the SCSI controller.

#### 5.1.2 NARROW BASED DISK DRIVES

Narrow Based SCSI Disk Drives may be attached to the buss but must always be the last in the chain after any Wide disks are connected.

A quality 68 pin to 50 pin Adaptor must be used when connecting from the SCSI buss to a narrow disk drive.

The end of the Buss must be terminated using an active SCSI terminator plugged into the last drive in the chain.

When using narrow based disk drives along the SCSI buss the Engine must be configured for Narrow use only.



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#### 5.1.2 CONFIGURING THE ENGINE FOR NARROW USE

Connect disk drives to external SCSI buss port.

Power on the disk drives and Engine

When the BIOS screen appears PRESS THE SPACE BAR, before the -

"Press Key To Display Boot Menu" Countdown ends.

Under Booting Procedure press RC and press Enter .

A message should appear next on screen asking

"Do you want to use the recommendedNVRAM Configuration" PRESS N

Press C then Enter - to "Reconfigure The System Setup"

Press Return untill the cursor has moved down to the 11 th line -

Slot 1 SCSI Wide.....

At this point you will need to know which PCI slot the SCSI Controller Card has been fitted in. Looking at the Merlin Engine from the rear will show which slot the card is in -

- Slot 1 is the top most slot
- Slot 2 is the middle slot
- Slot 3 is the Bottom slot

Use the Enter key on the Console Keyboard to move down the list to the correct slot.

Next press 0 on the Console Keyboard to change from Wide to Narrow use.

Then Press Enter untill the following message appears on screen

"Enter the NVRAM Section you wish to Update"

Select Q and then press Enter.

A message will appear -

"NVRAM Configuration is complete ....System will now restart"

Press Return on the Console Keyboard.

The configuration is now complete.

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#### 5.1.3 MIXING WIDE AND NARROW DISK DRIVES

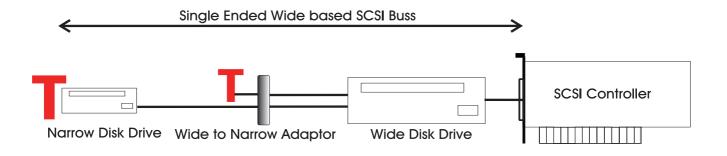
Wide and Narrow based disk drives can be mixed on the same SCSI buss at the same time -

Narrow Based SCSI Disk Drives must always be the last in the chain after any Wide disks are connected.

If the Engine is configured to run in Wide Mode, a quality 68 pin to 50 pin Adaptor must be used when connecting from the SCSI buss to a narrow disk drive.

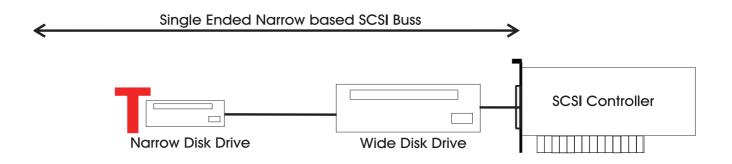
Please note the High Data Byte must be terminated when using an Adaptor, to allow for correct termination wide converting to a Narrow disk drive.

Remember to always terminate the last drive in the chain.



If the Engine is configured to run in Narrow mode, an Adaptor without High Byte termination may be used.

Remember to always terminate the last drive in the chain.



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## 5.2 CHECKING FOR NEWLY INSTALLED SCSI DEVICES

Once you have mounted all external and internal SCSI devices, the following will aid you in determining if they are all detected.

1. Ensure that the Engine is completely reassembled and that there are no loose cables.

2. Power up the Engine and Console.

3. Press the SPACE BAR once a gray display is seen. If you miss the time window in which the Space bar must be pressed, simply reboot and try again.

4. The displayed prompt is " Select a boot method from the above menu: ".

5. From the prompt type " SCAN " < RETURN >.

6. Observe that all SCSI devices are detected.

7. You may need to run this command a couple of times as some drives are much slower to boot than the Engine.

8. If a SCSI device is not seen, power down the system and check all SCSI ID's and that the SCSI and power cables are connected.

### 5.3 SETTING UP REMOVABLE MEDIA DEVICES

To set up optical drives for use on the system it is not necessary to format the media. All that is required is that the following command be run on the media;

DISKINIT /tdwx -w -n=name

(W is the PCI #, X is the SCSI ID)

This command must be run from the shell, so you will need to quit from the main application to the shell.

When media is not in use it is strongly recommended that the media is ejected from the drive to ensure that the media has not deteriorated due to the level of heat in the drive itself.

# CAUTION - KEEP MEDIA AWAY FROM STRONG MAGNETIC FIELDS AND DIRECT SUN LIGHT AS THIS CAN DAMAGE THE DISK.

Please note - The system automatically detects new SCSI devices. For example if an Optical drive is connected at boot up and the media is not present the device will be seen on the Project page, however it will indicate no media. On placing a suitable formatted media in the drive the device will become available for use.

Hard drives will also be automatically detected if they are installed correctly.

Please avoid connecting external devices by breaking the SCSI chain while the system is running as it can either crash the system or if a project is open, lead to project corruption.

# SECTION 6 - ROUTINE MAINTENANCE

## 6.1 CLEANING THE EXTERIOR OF THE UNITS

You should clean the MFX3.48 system units often enough to prevent dust or dirt from accumulating. Dirt acts as a thermal insulating blanket that prevents effective heat dissipation and may provide high-resistance electrical leakage paths between conductors or components in a humid environment.

Clean the dust from the outside by wiping with a soft cloth or small brush. A brush is especially useful for removing dust from around connectors and cooling grilles. Use a cloth dampened in water that contains 50% Isopropyl alcohol to remove hardened dirt. You should not use abrasive cleaners.

### 6.2 CLEANING THE MONITOR SCREENS

The Graphics Monitor has a special coating on the screen which prevents glare. A cleaning cloth should be supplied with the Graphics Monitor and the instructions given in the monitor manual should be followed.

## 6.3 CLEANING THE ENGINE AIR FILTER

To ensure good airflow through the Engine, the air filter in the front panel must be periodically cleaned. It is usually sufficient to simply remove accumulated dust using a vacuum cleaner to suck it out of the front panel grille. From time to time, however, the front panel will have to be removed to give the filter a thorough clean or even replace it altogether. The front dress panel should removed to allow access to the Air Filter.

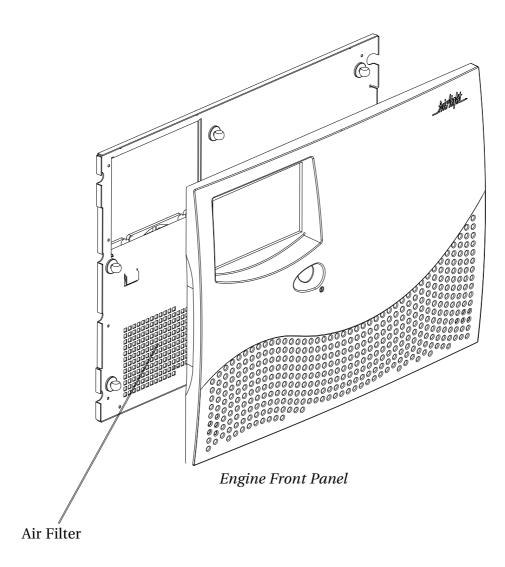
- 1. Gently lift the dress panel away from the Engine.
- 2. Unclip the Air Filter and remove it from the front facia of the 2nd panel.

3. Once the air filter has been removed, clean it thoroughly by washing it in warm water with mild detergent. Make sure it is completely dry before reinstalling it.

CAUTION: DO NOT ALLOW WATER TO GET INSIDE ANY ENCLOSED ASSEMBLY OR COMPONENT. DO NOT CLEAN ANY PLASTIC MATERIALS WITH ORGANIC CLEANING SOLVENTS, SUCH AS BENZENE, TOLUENE, XYLENE, ACETONE, OR SIMILAR COMPOUNDS, BECAUSE THEY MAY DAMAGE THE PLASTIC. CAUTION: DO NOT USE CLEANING FLUIDS, OTHER THAN THOSE MENTIONED IN THE GRAPHICS MONITOR MANUAL, TO CLEAN THE SCREEN - THE SPECIAL COATING COULD BE DAMAGED.

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4. Reverse the above steps to attach the front dress panel back into place.



#### New Air Filters

If the filter cannot be cleaned properly or is worn out, a new one should be ordered. Please contact your local Fairlight office or Distributor.

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# **APPENDIX - CONNECTION SPECIFICATIONS**

#### INTRODUCTION

The following information contains all the wiring details to allow users to connect peripheral devices to the back panel of the Engine.

## A1 - AUDIO I/O CONFIGURATION

MFX3.48 uses a Fairlight ESP designed proprietary card called the QDC for all its audio I/O and processing. A number of QDC cards can be configured within a Engine. MFX3.48 can be ordered with any of 12 Audio I/O combinations.

On each QDC , a maximum of 6 I/O module boards, can be configured, to allow  $\,$  8 I/O channels per module. This allows for a maximum of -

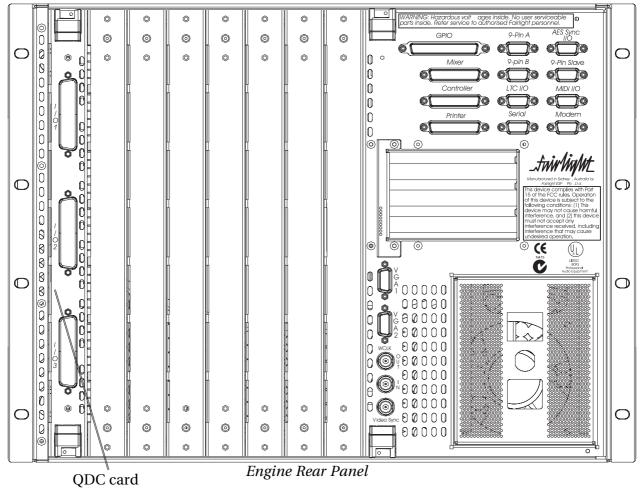
16 analog inputs and 32 analog outputs

or

32 digital inputs and 32 digital outputs

16 analog inputs per QDC to be available.

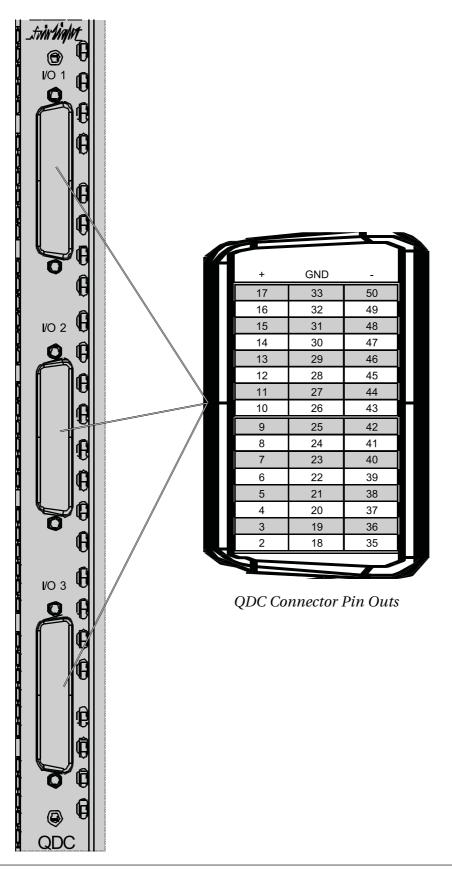
The following pages describe the wiring pin outs for each QDC, to enable installation personnel to configure and wire the system.



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## **QDC** CONFIGURATION

Each QDC card comes with three connectors, each housing 50 pins. The following tables show the Audio I/O Configurations - the connectors and Pinouts for MFX3.48. In some configurations more than one QDC card is required to enable all analog and digital I/O.



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# A1.1 ANALOG AUDIO I/O SPECIFICATIONS

Connector	50 way 3 Row D Female
Input	Balanced
Input Level	+24dbu max
Input Sensitivity	+4dbu nominal (adjustable)
Input Impedence	> 10k Ohms
Output	Electronic balanced differential
Output Level	+24dbu max at 0dBFS, nominal +4dbu
Output Impedence	< 55 Ohms
Output Load	>600 Ohms

## A1.2 DIGITAL AUDIO I/O SPECIFICATIONS

Connector	50 way 3 row D Female
Channels	16 stereo pairs per I/O Module
Sample Rates	32KHz, 44.1 KHz, 48Khz, 88.2KHz, 96 KHz,
	0.1% run up and run down
Input Type	> 200mV differential
Output level	>4.3V TTL



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16 Track Digital 16 Digital I/Os 1 QDC Card

# mfx3.48

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1 QDC Card 8 Analog Inputs 16 Digital I/Os **16 Track Mixed** 



1 QDC Card
16 Digital I/Os
<b>16 Analog Outputs</b>
8 Analog Inputs
16 Track Mixed

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Pins         Port           GND         -         A Out           33         50         -         A Out           33         50         47         A Out           33         50         47         A Out           33         50         47         A Out           29         46         A Out         22           33         50         40         A Out           27         44         A Out         23           33         50         A Out         23           33         50         A Out         23           33         50         A Out         23           33         51         48         A Out           33         50         A Out         23           347         A Out         22         43         A Out           26         43         A Out         26         43           26         43         A Out         26         40
Pins         Port           GND         -         A Out           33         50         -         A Out           33         50         47         A Out           33         50         47         A Out           33         50         47         A Out           29         46         A Out         22           33         50         40         A Out           27         44         A Out         23           33         50         A Out         23           33         50         A Out         23           33         50         A Out         23           33         51         48         A Out           33         50         A Out         23           347         A Out         22         43         A Out           26         43         A Out         26         43           26         43         A Out         26         40
Pins         Port           GND         -         A Out           33         50         -         A Out           33         50         -         A Out           33         50         45         A Out           33         50         45         A Out           29         46         A Out         22           33         50         40         A Out           27         44         A Out         23           33         50         A Out         23           33         50         A Out         23           33         50         A Out         23           31         48         A Out         23           33         50         A Out         23           34         A Out         22         43           26         43         A Out         22           26         43         A Out         20           26         43         A Out         20
Pins         Port           GND         -         A Out           33         50         -         A Out           33         50         -         A Out           33         50         45         A Out           33         50         45         A Out           29         46         A Out         22           33         50         40         A Out           27         44         A Out         23           33         50         A Out         23           33         50         A Out         23           33         50         A Out         23           31         48         A Out         23           33         50         A Out         23           34         A Out         22         44           26         43         A Out         22           26         43         A Out         20           26         43         A Out         20
Pins         Port           GND         -         A Out           33         50         -         A Out           33         50         -         A Out           33         50         45         A Out           31         48         A Out         29           33         50         45         A Out           23         45         A Out         33           33         50         A Out         33           34         A Out         33         50           33         50         A Out         33           34         A Out         34         A Out           25         43         A Out         22         40           26         43         A Out         20         40           26         43         A Out         20         40
Pins 33 2 49 33 47 26 43 33 50 33 47 28 45 28 45
Pins 33 33 33 33 33 33 33 33 33 33 33 33 33
Pins 33 33 33 33 33 33 33 33 33 33 33 33 33
Analog Inputs aDC Con 1 1 1 1 1 1 1 1 1 1 1 1 1
Analog Inpu abc Con 1 3 3 4
abc c abc c
abc
Port A III 1 A

16 Track Mixed 16 Analog Inputs 16 Analog Outputs 16 Digital I/Os 1 QDC Card



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			•	46	45	44	43	38	37	36	35	38	37	36	35
		Pins	+ GND	29	28	27	26	21	20	19	18	21	20	19	18
	ts		+	13	12	÷	9	ß	4	ო	2	S	4	e	2
	utpu	Con		2				2				e			
	Digital Outputs	QDC		-				-				-			
	Dig	Port		D Out 1 - 2	D Out 3 - 4	D Out 5 - 6	D Out 7 - 8	D Out 9 - 10	D Out 11 - 12	D Out 13 - 14	D Out 15 - 16	D Out 17 - 18	D Out 19 - 20	D Out 21 - 22	D Out 23 - 24
			•	50	49	48	47	42	41	40	39	42	41	40	39
		Pins	+ GND			<del>ت</del>	30	25	24	23	22	25	24	23	22
	(0		+	17 33	16 32	15	14	6	œ	~	9	6	œ	~	9
	nputs	Con		7				7				e			
7	Digital Inputs	QDC Con		-				-				-			
1 QDC Card	Diç	Port		D In 1 - 2	D In 3 - 4	D In 5 - 6	D In 7 - 8	D In 9 - 10	D In 11 - 12	D In 13 - 14	D In 15 - 16	D In 17 - 18	D In 19 - 20	D In 21 - 22	D In 23 - 24
-															
S	ts	Pins	+ GND +												
24 Digital I/Os	Analog Outputs	Con	+												
gite	og C	QDC Con													
D	nalo														
24	A	Port													
le															
Digital	s	Pins	+ GND -												
24 Track Dig	Analog Inputs	Con													
24 T	nalog	QDC Con													
	A	Port													

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Analog Vo         24 Digital I/Os         8 Analog Inputs         1 GDC Card           Pins         Port         ODC         Port         ODC         Port         ODC         Port         Analog Outputs           Pins         Port         ODC         Port         ODC         Port         ODC         Port         ODC         Port         ODC         Port         Analog Outputs         Port         ODC         Port         ODC         Port         Analog Outputs         Port         ODC         Port         Analog Outputs         Port         Analog Port         Port         Analog Port         Anal					46	45	44	43	38	37	36	35	38	37	36	35
Analog Outputs         Banalog Inputs         I GDC Card           Pins         Port         ODC         Con         Pins         Port         ODC         Con         Pins         Port         ODC         Port         ODC         Analog Outputs         Port         ODC         Analog Outputs         Port         ODC         Analog Outputs         Port         ODC         Port         ODC         Port         ODC         Analog Outputs         Analog Outputs         Port         ODC         Analog Outputs         Port         ODC         Analog Outputs         Analog Outputs         Port         ODC         Analog Outputs         Analog Outputs         Port         ODC         Analog Outputs         Analog An			ins	Ŋ												18 3
Analog Outputs       Analog Outputs       B Analog Inputs       1 GDC         Pins       Port       aDC       Digital Inputs       Port         RND -       Analog Outputs       Port       aDC       Port       aDC       Port         33 50       Port       aDC       Con       Pins       Port       aDC       Port       Port         33 50       Port       aDC       Con       Pins       Port       aDC       Port       Port         32 49       Port       aDC       Con       Pins -       + GND -       Pin -       + GND -       Port       22 49       Pout13         32 44       Port       aDC       DIn -2       1       2       14 30 47       Dout13         32 44       Pont -1       2       1       2       9       25 42       Dout13         22 44       DIn 7-8       1       2       9       25 42       Dout13         26 43       DIn 11-12       1       2       9       25 42       Dout13         26 43       DIn 17-18       1       3       9       25 42       Dout13         26 43       DIn 17-18       1       3       9       25 42		S	а.	+	13		=		ß	4	e	2	ß	4	e	2
Analog Outputs       Analog Outputs       B Analog Inputs       1 GDC         Pins       Port       aDC       Digital Inputs       Port         RND -       Analog Outputs       Port       aDC       Port       aDC       Port         33 50       Port       aDC       Con       Pins       Port       aDC       Port       Port         33 50       Port       aDC       Con       Pins       Port       aDC       Port       Port         32 49       Port       aDC       Con       Pins -       + GND -       Pin -       + GND -       Port       22 49       Pout13         32 44       Port       aDC       DIn -2       1       2       14 30 47       Dout13         32 44       Pont -1       2       1       2       9       25 42       Dout13         22 44       DIn 7-8       1       2       9       25 42       Dout13         26 43       DIn 11-12       1       2       9       25 42       Dout13         26 43       DIn 17-18       1       3       9       25 42       Dout13         26 43       DIn 17-18       1       3       9       25 42		utput	Con		0				0				e			
Analog Outputs       Analog Outputs       B Analog Inputs       1 GDC         Pins       Port       aDC       Digital Inputs       Port         RND -       Analog Outputs       Port       aDC       Port       aDC       Port         33 50       Port       aDC       Con       Pins       Port       aDC       Port       Port         33 50       Port       aDC       Con       Pins       Port       aDC       Port       Port         32 49       Port       aDC       Con       Pins -       + GND -       Pin -       + GND -       Port       22 49       Pout13         32 44       Port       aDC       DIn -2       1       2       14 30 47       Dout13         32 44       Pont -1       2       1       2       9       25 42       Dout13         22 44       DIn 7-8       1       2       9       25 42       Dout13         26 43       DIn 11-12       1       2       9       25 42       Dout13         26 43       DIn 17-18       1       3       9       25 42       Dout13         26 43       DIn 17-18       1       3       9       25 42	þ	ital O	QDC		-				-				-			
Analog Outputs       B Analog Inputs         Pins       Port       ODC       Con       Pins         Pins       Port       ODC       Con       Pins         RND -       Bort       ODC       Con       Pins         State       Port       ODC       Con       Pins         RND -       Bort       ODC       Con       Pins         State       Bort       ODC       Con       Pins         33<50	I QDC Cal	Dig	Port		D Out 1 - 2	D Out 3 - 4	D Out 5 - 6	D Out 7 - 8	D Out 9 - 10	D Out 11 - 12	D Out 13 - 14	D Out 15 - 16	D Out 17 - 18	D Out 19 - 20	D Out 21 - 22	D Out 23 - 24
Analog Outputs       B Analog Inputs         Pins       Port       ODC       Con       Pins         Pins       Port       ODC       Con       Pins         RND       Analog Outputs       Port       ODC       Con       Pins         RND       Port       ODC       Con       Pins       Port       ODC       Con       Pins         RND       Port       ODC       Con       Pins       Port       ODC       Con       Pins         3       50       Port       ODC       Con       Pins       Port       ODC       Port       AGN       A GND         3       50       Dins - 10       1       2       16       32       21       33       24       33       34       33       34       33       34       33       34       33       34       33       34       34       36       31       34       33       34       33       34       33       34       34       34       36       35       34       33       34       34       34       36       35       34       34       34       34       34       34       34       34       34	-				20	49	48	47	42	41	40	39	42	41	40	39
Analog Outputs         Analog Inputs         B Analog Inputs           Pins         Analog Outputs         Digital Inputs           Pins         Port         aDC         con         Pins         Port         aDC         con           33<50         -         +         GND         -         Digital Inputs         +         <			ins	Ŋ												ដ
Analog       24 Digital I/Os       8 An         Pins       Analog Outputs       Port         Pins       Port       ODC       Con       Pins       Port         GND       -       Analog Outputs       Port       Port       Port         33<50       -       + GND       -       Din 3 - 4         33<50       -       -       Port       Port         341       -       -       Din 3 - 4       Din 3 - 4         29<46       -       -       Din 3 - 4       Din 7 - 8         29<46       -       -       Din 7 - 8       Din 7 - 8         26<43       -       -       Din 7 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1			ш.	+	17	16	15	14	6	œ	~	9	6	œ	~	9
Analog       24 Digital I/Os       8 An         Pins       Analog Outputs       Port         Pins       Port       ODC       Con       Pins       Port         GND       -       Analog Outputs       Port       Port       Port         33<50       -       + GND       -       Din 3 - 4         33<50       -       -       Port       Port         341       -       -       Din 3 - 4       Din 3 - 4         29<46       -       -       Din 3 - 4       Din 7 - 8         29<46       -       -       Din 7 - 8       Din 7 - 8         26<43       -       -       Din 7 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1	puts	nputs	Con		7				0				e			
Analog       24 Digital I/Os       8 An         Pins       Analog Outputs       Port         Pins       Port       ODC       Con       Pins       Port         GND       -       Analog Outputs       Port       Port       Port         33<50       -       + GND       -       Din 3 - 4         33<50       -       -       Port       Port         341       -       -       Din 3 - 4       Din 3 - 4         29<46       -       -       Din 3 - 4       Din 7 - 8         29<46       -       -       Din 7 - 8       Din 7 - 8         26<43       -       -       Din 7 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1         26<43       -       -       Din 17 - 1       Din 17 - 1	og In	gital I	QDC		-				-				-			
Anal           Pins         Anal           Pins         Port         Q           33<50         -         33<50           31<48         31<48         31<48           32<49         -         -         22           29<46         -         22         44           26<43         -         -         22           26<43         -         -         -	8 Anal	Di	Port		D In 1 - 2	D In 3 - 4	D In 5 - 6	D In 7 - 8	D In 9 - 10	D In 11 - 12	D In 13 - 14	D In 15 - 16	D In 17 - 18	D In 19 - 20	D In 21 - 22	D In 23 - 24
Anal           Pins         Anal           Pins         Port         Q           33<50         -         33<50           31<48         31<48         31<48           32<49         -         -         22           29<46         -         22         44           26<43         -         -         22           26<43         -         -         -	s															
Anal           Pins         Anal           Pins         Port         Q           33<50         -         33<50           31<48         31<48         31<48           32<49         -         -           29<46         -         -           26<43         -         -           26<43         -         -	0/1		oins	UN2												
Anal           Pins         Anal           Pins         Port         Q           33<50         -         33<50           31<48         31<48         31<48           32<49         -         -           29<46         -         -           26<43         -         -           26<43         -         -	tal	uts	-	+												
Anal           Pins         Anal           Pins         Port         Q           33<50         -         33<50           31<48         31<48         31<48           32<49         -         -           29<46         -         -           26<43         -         -           26<43         -         -	Digi	Outp	Con													
<b>1ixed I/O</b> Pins Port GND - 33 50 32 49 31 48 33 47 29 46 28 45 26 43 26 43	24	nalog	QDC													
k Mixed I/C uts Pins + GND - 17 33 50 16 32 49 15 31 48 15 31 48 15 32 49 15 28 45 11 27 44 10 26 43	•	A	Port													
k Mixed uts + GND - 17 33 56 16 32 49 15 31 48 15 31 48 15 32 49 11 22 49 11 22 44 11 22 44 11 22 44					_	_	_	_			_	_	_		_	
H M M H M H M H M H M H M H M H M H M H	xec		su	9												
¥ 5 7777777	M	S	Ρ	₽ 4												
	rack	Input	Con	•	-			-	-	-	-	-				
<b>24 Track N</b> Analog Inputs abc Con 1 1 15 15 15 15 15 15 15 15 15 15 15 15 15	24 T	nalog			-											
A Port A In 1 A In 2 A In 2 A In 5 A In 7 A In 8		A	Port		A In 1	A In 2	A In 3	A In 4	A In 5	A In 6	A In 7	2				



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2 QDC Cards	Digital Outputs	Port QDC Con Pins	+ GND -																							
2 QD(		Pins	+ GND -																							
outputs	Digital Inputs	QDC Con																								
24 Analog Outputs	Dig	Port																								
24						_																				
			•	42	41	39	38	37	36	35	42	4	40	39	38	37	36	35	50	49	48	47	46	45	44	43
					- ~			~						~	_	~	~							~	•	<i>(</i>
	s	Pins	GND	25	24	52	21	20	19	18	25	24	23	22	21	20	19	18	33	32	31	30	29		1 27	0 26
ts	tputs		+ GND	9 25	8 24			4 20						6 22	5 21	4 20	3 19	18			31	30	29		11 27	10 26
puts	Outputs	Con Pins		1 9 25	8 24	52	21		19	18	25	24						18	33	32	31	30	29			
J Inputs	og Outputs	Con		1 1 9 25	8 24	52	21		19	18	9 25	24						18	17 33	32	31	30	29			
log Inputs	nalog Outputs			1 1 9 25	8 24	6 22	5 21	4	3 19	2 18	1 3 9 25	8 24	7 23	9	2	4	3	2 18	1 2 17 33	16 32	15 31	14 30	13 29	12	÷	10
Analog Inputs	Analog Outputs	QDC Con		1 1 9	4 03	6 22	5 21	4	3 19	2 18	1 3 9 25	8 24	7 23	9	2	4	3	2 18	1 2 17 33	16 32	15 31	14 30	13 29	12	÷	10
24 Analog Inputs	Analog Outputs	Con		1 1 9	A Out 2 8 24	6 22	5 21		3 19	2 18	1 3 9 25	Out 10 8 24		Out 12 6	Out 13 5			Out 16 2 18	1 2 17 33	32	31	14 30	Out 21 13 29	12		
24 Analog Inputs	Analog Outputs	QDC Con		A Out 1 1 1 9	4 03	A Out 4 6 22	5 21	A Out 6 4	A Out 7 3 19	2 18	A Out 9 1 3 9 25	A Out 10 8 24	A Out 11 7 23	A Out 12 6	Out 13 5	A Out 14 4	3	A Out 16 2 18	1 2 17 33	A Out 18 16 32	A Out 19 15 31	A Out 20 14 30	13 29	A Out 22 12	A Out 23 11	A Out 24 10
24 Analog Inputs	Analog Outputs	ns Port QDC Con	+ - DN	50 A Out 1 1 1 9	A Out 2 8	A Out 4 6 22	A Out 5 21	A Out 6 4	A Out 7 3 19	A Out 8 2 18	A Out 9 1 3 9 25	A Out 10 8 24	A Out 11 7 23	A Out 12 6	A Out 13 5	A Out 14 4	A Out 15 3	A Out 16 2 18	A Out 17 1 2 17 33	A Out 18 16 32	A Out 19 15 31	A Out 20 14 30	A Out 21 13 29	45 A Out 22 12	A Out 23 11	10
		Port QDC Con	+	33 50 A Out 1 1 9	49 A Out 2 8	30 47 A Out 4 6 22	29 46 A Out 5 21	45 A Out 6 4	27 44 A Out 7 3 19	26 43 A Out 8 2 18	50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6	29 46 A Out 13 5	28 45 A Out 14 4	27 44 A Out 15 3	26 43 A Out 16 2 18	33 50 A Out 17 1 2 17 33	32 49 A Out 18 16 32	31 48 A Out 19 15 31	30 47 A Out 20 14 30	29 46 A Out 21 13 29	28 45 A Out 22 12	27 44 A Out 23 11	43 A Out 24 10
		Pins Port QDC Con	GND - G	33 50 A Out 1 1 9	32 49 A Out 2 8	30 47 A Out 4 6 22	29 46 A Out 5 21	28 45 A Out 6 4	27 44 A Out 7 3 19	26 43 A Out 8 2 18	33 50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6	29 46 A Out 13 5	28 45 A Out 14 4	27 44 A Out 15 3	26 43 A Out 16 2 18	33 50 A Out 17 1 2 17 33	32 49 A Out 18 16 32	31 48 A Out 19 15 31	30 47 A Out 20 14 30	29 46 A Out 21 13 29	28 45 A Out 22 12	27 44 A Out 23 11	26 43 A Out 24 10
		Con Pins Port QDC Con	GND - G	33 50 A Out 1 1 9	32 49 A Out 2 8	30 47 A Out 4 6 22	29 46 A Out 5 21	28 45 A Out 6 4	27 44 A Out 7 3 19	26 43 A Out 8 2 18	17 33 50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6	29 46 A Out 13 5	28 45 A Out 14 4	27 44 A Out 15 3	26 43 A Out 16 2 18	1 17 33 50 A Out 17 1 2 17 33	32 49 A Out 18 16 32	31 48 A Out 19 15 31	30 47 A Out 20 14 30	29 46 A Out 21 13 29	28 45 A Out 22 12	27 44 A Out 23 11	26 43 A Out 24 10
		Pins Port QDC Con	GND - G	33 50 A Out 1 1 9	32 49 A Out 2 8	30 47 A Out 4 6 22	29 46 A Out 5 21	28 45 A Out 6 4	27 44 A Out 7 3 19	26 43 A Out 8 2 18	17 33 50 A Out 9 1 3 9 25	16 32 49 A Out 10 8 24	15 31 48 A Out 11 7 23	14 30 47 A Out 12 6	13 29 46 A Out 13 5	12 28 45 A Out 14 4	11 27 44 A Out 15 3	10 26 43 A Out 16 2 18	33 50 A Out 17 1 2 17 33	32 49 A Out 18 16 32	31 48 A Out 19 15 31	30 47 A Out 20 14 30	29 46 A Out 21 13 29	28 45 A Out 22 12	27 44 A Out 23 11	26 43 A Out 24 10
24 Track Analog 24 Analog Inputs	Analog Inputs Analog Outputs	Con Pins Port QDC Con	GND - G	In 1 1 1 17 33 50 A Out 1 1 1 9	32 49 A Out 2 8	In 4 14 30 47 A Out 4 6 22	5 13 29 46 A Out 5 5 21	In 6 12 28 45 A Out 6 4	7 11 27 44 A Out 7 3 19	26 43 A Out 8 2 18	1 3 17 33 50 A Out 9 1 3 9 25	16 32 49 A Out 10 8 24	15 31 48 A Out 11 7 23	14 30 47 A Out 12 6	13 29 46 A Out 13 5	12 28 45 A Out 14 4	27 44 A Out 15 3	10 26 43 A Out 16 2 18	2 1 17 33 50 A Out 17 1 2 17 33	16 32 49 A Out 18 16 32	15 31 48 A Out 19 15 31	14 30 47 A Out 20 14 30	13 29 46 A Out 21 13 29	12 28 45 A Out 22 12	11 27 44 A Out 23 11	10 26 43 A Out 24 10

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24 Digital I/Os – 24 Analog Inputs – 24 Analog Outputs	Digital Inputs Digital Outputs	Port QDC Con Pins Port QDC	+ GND -	DIn 1 - 2 1 2 17 33 50 D Out 1 - 2	D ln 3 - 4 16 32 49 D Out 3 - 4	D In 5 - 6 15 31 48 D Out 5 - 6	D In 7 - 8 14 30 47 D Out 7 - 8	D ln 9 - 10 1 2 9 25 42 D Out 9 - 10	24	40	6 22 39	2 2 17	16 32 49	D In 21 - 22 15 31 48 D Out 21 - 22	In 23 - 24 14 30 47 D Out 23 - 24												
Digital I/Os	puts	n Pins	+ GND -	9 25 42	8 24 41	7 23 40	39	5 21 38 1			2 18 35 D	9 25 42 D	41	40									6 22 39		4 20 37	3 19 36	2 18 35
24 Track Analog plus Digital 24	Analog Outputs	Port QDC Con		A Out 1 1 1	A Out 2	A Out 3	A Out 4	A Out 5	A Out 6	A Out 7	A Out 8	A Out 9 1 3	A Out 10	A Out 11	A Out 12	A Out 13	A Out 14	A Out 15	A Out 16	A Out 17 2 1	A Out 18	A Out 19	A Out 20	A Out 21	A Out 22	A Out 23	A Out 24



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			•		45	44	43	38	37	36	35	38	37	36	35	46	45	44	43
		Pins	GND	29	28	27	26	5	20	19	18	21	20	19	18	29	28	27	26
	ts		+	13	12	÷	9	5	4	ო	2	2	4	ო	2	13	12	÷	10
	utpu	Con		7				0				ო				-			
	Digital Outputs	QDC		-				-				-				2			
	Dig	Port		D Out 1 - 2	D Out 3 - 4	D Out 5 - 6	D Out 7 - 8	D Out 9 - 10	D Out 11 - 12	D Out 13 - 14	D Out 15 - 16	D Out 17 - 18	D Out 19 - 20	D Out 21 - 22	D Out 23 - 24	D Out 25 - 26	D Out 27 - 28	D Out 29 - 30	D Out 31 - 32
			•	50	49	48	47	42	41	40	39	42	41	40	39	50	49	48	47
		Pins	GND	ŝ	32	3	30	25	24	33		25	24	23	22	33	32	3	30
	S	-	+	17	16	15	14 14	6	œ	~	9	6	8	~	9	17	16	15	14
ls	nput	Con		0				0				ო				-			
Caro	Digital Inputs	QDC		-				-				-				0			
2 QDC Cards	Di	Port		D In 1 - 2	D In 3 - 4	D In 5 - 6	D In 7 - 8	D In 9 - 10	D In 11 - 12	D In 13 - 14	D In 15 - 16	D In 17 - 18	D In 19 - 20	D In 21 - 22	D In 23 - 24	D In 25 - 26	D In 27 - 28	D In 29 - 30	D In 31 - 32
S	ts	Pins	+ GND -																
al I/Os	Outputs		+ GND -																
Digital I/Os	nalog Outputs	QDC Con Pins	+ GND -																
32 Digital I/Os	Analog Outputs	Con	+ GND +																
	Analog Outputs	QDC Con	+ GND -																
Ital		QDC Con																	
Ital		Pins Port QDC Con	+ GND - + GND -																
Ital		Pins Port QDC Con																	
		QDC Con Pins Port QDC Con																	
Ital	Analog Inputs Analog Outputs	Pins Port QDC Con																	

32 Track Digital 32 Digital I/Os 2 QDC Cards

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1	Analog Inputs	g Inp	outs		An	alog	Analog Outputs	its	Di	Digital Inputs	nput	S		Digi	ital O	Digital Outputs	ts		
Port	QDC	Con	Pins		Port	adc	Con	Pins	Port	QDC	Con	α.	Pins	Port	adc	Con	а	Pins	
			+ GND -					+ GND -				5 +	GND -				+	GND	
A In 1	-	-							D In 1 - 2	-	2	17	33 50	D Out 1 - 2	-	7	13	29 4	46
A In 2									D In 3 - 4			16	32 49	D Out 3 - 4			12	28 4	45
A In 3				_					D In 5 - 6			15	31 48	D Out 5 - 6			÷	27 4	44
A In 4									D In 7 - 8			4	30 47	D Out 7 - 8			9	26 4	43
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A In 15									D In 29 - 30			~	23 40	D Out 29 - 30			e	19 3	36
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32 Track Mixed I/O 32 Digital I/O – 16 Analog Inputs 1 QDC Card



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32 Track Analog 32 Anal		Con Pins Port QDC	GND	33 50 A	32 49 A	31 48 A	30 47 A	29 46 A	28 45	27 44	17 33 50 A Out 8	16 32 49 A Out 10	31 48	30 47	29 46	28 45	27 44	10 26 43 A Out 16	33 50 A Out 17 1	32 49	31 48	30 47	29 46	28 45	27 44 26 43	17 33 50 A Out 25 1	16 32 49 A Out 26	31 48 A Out 27	30 47 A Out 28	29 46	28 45 A	27 44	26 43
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32 Digital I/Os – 32 Analog Inputs – 32 Analog Outputs	Outputs	Con Pins	GND																																2 18 35
	og Outputs	Con	GND									9 25								1 9 25								3 9 25							2 18 35
	alog Outputs		GND									9 25																9 25							2 18 35
	Analog Outputs	QDC Con	GND	1 1 9 25	8 24	7 23	6 22	5 21	4 20	3 19	2 18	1 3 9 25	8 24	7 23	6 22	5 21	4 20	3 19	2 18	2 1 9 25	8 24	7 23	6 22	5 21	4 20	3 19	2 18	2 3 9 25	8 24	7 23	6 22	5 21	4 20	3 19	2 18
	Analog Outputs	Con	GND	Out 1 1 1 9 25	8 24	7 23	6 22	Out 5 21 5 21	4 20	3 19	2 18	1 3 9 25	8 24	7 23	Out 12 6 22	Out 13 5 21	4 20	3 19	2 18	2 1 9 25	8 24	Out 19 7 23	Out 20 6 22	5 21	4 20	3 19	Out 24 2 18	Out 25 2 3 9 25	Out 26 8 24	Out 27 7 23	Out 28 6 22	5 21	4 20	Out 31 3 19	Out 32 2 18
	Analog Outputs	QDC Con	GND	1 1 9 25		7 23	6 22	Out 5 21 5 21	4 20	3 19	2 18	1 3 9 25			6 22	5 21	4 20		2 18	1 9 25		7 23	6 22	5 21	4 20	3 19	2 18	2 3 9 25	8 24	7 23	6 22	5 21	4 20	Out 31 3 19	2 18
	Analog Outputs	Port QDC Con	+ GND	Out 1 1 1 9 25	8 24	A Out 3 7 23	A Out 4 6 22	A Out 5 21	A Out 6 4 20	A Out 7 3 19	A Out 8 2 18	A Out 9 1 3 9 25	8 24	7 23	A Out 12 6 22	Out 13 5 21	A Out 14 4 20	A Out 15 3 19	A Out 16 2 18	2 1 9 25	A Out 18 8 24	A Out 19 7 23	A Out 20 6 22	A Out 21 5 21	A Out 22 4 20	A Out 23 3 19	A Out 24 2 18	Out 25 2 3 9 25	Out 26 8 24	Out 27 7 23	Out 28 6 22	A Out 29 5 21	A Out 30 4 20	A Out 31 3 19	Out 32 2 18
	Analog Outputs	Port QDC Con	+ GND	50 A Out 1 1 1 9 25	49 A Out 2 8 24	48 A Out 3 7 23	47 A Out 4 6 22	A Out 5 21	45 A Out 6 4 20	44 A Out 7 3 19	43 A Out 8 2 18	50 A Out 9 1 3 9 25	49 A Out 10 8 24	48 A Out 11 7 23	47 A Out 12 6 22	46 A Out 13 5 21	A Out 14 4 20	44 A Out 15 3 19	43 A Out 16 2 18	50 A Out 17 2 1 9 25	49 A Out 18 8 24	A Out 19 7 23	47 A Out 20 6 22	46 A Out 21 5 21	45 A Out 22 4 20	44 A Out 23 3 19	43 A Out 24 2 18	50 A Out 25 2 3 9 25	49 A Out 26 8 24	A Out 27 7 23	47 A Out 28 6 22	46 A Out 29 5 21	45 A Out 30 4 20	44 A Out 31 3 19	A Out 32 2 18
		QDC Con	+ GND	50 A Out 1 1 1 9 25	32 49 A Out 2 8 24	31 48 A Out 3 7 23	30 47 A Out 4 6 22	46 A Out 5 21	28 45 A Out 6 4 20	27 44 A Out 7 3 19	26 43 A Out 8 2 18	33 50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6 22	29 46 A Out 13 5 21	45 A Out 14 4 20	27 44 A Out 15 3 19	26 43 A Out 16 2 18	50 A Out 17 2 1 9 25	32 49 A Out 18 8 24	48 A Out 19 7 23	30 47 A Out 20 6 22	29 46 A Out 21 5 21	45 A Out 22 4 20	27 44 A Out 23 3 19	26 43 A Out 24 2 18	33 50 A Out 25 2 3 9 25	32 49 A Out 26 8 24	48 A Out 27 7 23	30 47 A Out 28 6 22	46 A Out 29 5 21	28 45 A Out 30 4 20	27 44 A Out 31 3 19	43 A Out 32 2 18
		Pins Port QDC Con	GND - + GND	33 50 A Out 1 1 1 9 25	32 49 A Out 2 8 24	31 48 A Out 3 7 23	30 47 A Out 4 6 22	29 46 A Out 5 5 21	28 45 A Out 6 4 20	27 44 A Out 7 3 19	26 43 A Out 8 2 18	17 33 50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6 22	29 46 A Out 13 5 21	28 45 A Out 14 4 20	27 44 A Out 15 3 19	26 43 A Out 16 2 18	33 50 A Out 17 2 1 9 25	32 49 A Out 18 8 24	31 48 A Out 19 7 23	30 47 A Out 20 6 22	29 46 A Out 21 5 21	28 45 A Out 22 4 20	27 44 A Out 23 3 19	26 43 A Out 24 2 18	17 33 50 A Out 25 2 3 9 25	32 49 A Out 26 8 24	31 48 A Out 27 7 23	30 47 A Out 28 6 22	29 46 A Out 29 5 21	28 45 A Out 30 4 20	27 44 A Out 31 3 19	26 43 A Out 32 2 18
		Con Pins Port QDC Con	GND - + GND	33 50 A Out 1 1 1 9 25	32 49 A Out 2 8 24	31 48 A Out 3 7 23	30 47 A Out 4 6 22	29 46 A Out 5 5 21	28 45 A Out 6 4 20	27 44 A Out 7 3 19	26 43 A Out 8 2 18	33 50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6 22	29 46 A Out 13 5 21	28 45 A Out 14 4 20	27 44 A Out 15 3 19	26 43 A Out 16 2 18	33 50 A Out 17 2 1 9 25	32 49 A Out 18 8 24	31 48 A Out 19 7 23	30 47 A Out 20 6 22	29 46 A Out 21 5 21	28 45 A Out 22 4 20	27 44 A Out 23 3 19	26 43 A Out 24 2 18	33 50 A Out 25 2 3 9 25	32 49 A Out 26 8 24	31 48 A Out 27 7 23	30 47 A Out 28 6 22	29 46 A Out 29 5 21	28 45 A Out 30 4 20	27 44 A Out 31 3 19	26 43 A Out 32 2 18
32 Track Analog plus Digital 32 Digital I/		Con Pins Port QDC Con	GND - + GND	33 50 A Out 1 1 1 9 25	32 49 A Out 2 8 24	31 48 A Out 3 7 23	30 47 A Out 4 6 22	29 46 A Out 5 5 21	28 45 A Out 6 4 20	27 44 A Out 7 3 19	26 43 A Out 8 2 18	17 33 50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6 22	29 46 A Out 13 5 21	28 45 A Out 14 4 20	27 44 A Out 15 3 19	26 43 A Out 16 2 18	33 50 A Out 17 2 1 9 25	32 49 A Out 18 8 24	31 48 A Out 19 7 23	30 47 A Out 20 6 22	29 46 A Out 21 5 21	28 45 A Out 22 4 20	27 44 A Out 23 3 19	26 43 A Out 24 2 18	17 33 50 A Out 25 2 3 9 25	32 49 A Out 26 8 24	31 48 A Out 27 7 23	30 47 A Out 28 6 22	29 46 A Out 29 5 21	28 45 A Out 30 4 20	27 44 A Out 31 3 19	26 43 A Out 32 2 18
	Analog Inputs Analog Outputs	Pins Port QDC Con	GND - + GND	33 50 A Out 1 1 1 9 25	16 32 49 A Out 2 8 24	15 31 48 A Out 3 7 23	14 30 47 A Out 4 6 22	13 29 46 A Out 5 5 21	12 28 45 A Out 6 4 20	11 27 44 A Out 7 3 19	10 26 43 A Out 8 2 18	1 3 17 33 50 A Out 9 1 3 9 25	16 32 49 A Out 10 8 24	15 31 48 A Out 11 7 23	14 30 47 A Out 12 6 22	13 29 46 A Out 13 5 21	12 28 45 A Out 14 4 20	11 27 44 A Out 15 3 19	10 26 43 A Out 16 2 18	2 1 17 33 50 A Out 17 2 1 9 25	16 32 49 A Out 18 8 24	15 31 48 A Out 19 7 23	14 30 47 A Out 20 6 22	13 29 46 A Out 21 5 21	12 28 45 A Out 22 4 20	11 27 44 A Out 23 3 19	10 26 43 A Out 24 2 18	2 3 17 33 50 A Out 25 2 3 9 25	16 32 49 A Out 26 8 24	15 31 48 A Out 27 7 23	14 30 47 A Out 28 6 22	13 29 46 A Out 29 5 21	12 28 45 A Out 30 4 20	11 27 44 A Out 31 3 19	10 26 43 A Out 32 2 18
		Con Pins Port QDC Con	GND - + GND	33 50 A Out 1 1 1 9 25	32 49 A Out 2 8 24	31 48 A Out 3 7 23	14 30 47 A Out 4 6 22	13 29 46 A Out 5 5 21	12 28 45 A Out 6 4 20	11 27 44 A Out 7 3 19	10 26 43 A Out 8 2 18	1 3 17 33 50 A Out 9 1 3 9 25	32 49 A Out 10 8 24	31 48 A Out 11 7 23	30 47 A Out 12 6 22	13 29 46 A Out 13 5 21	12 28 45 A Out 14 4 20	11 27 44 A Out 15 3 19	10 26 43 A Out 16 2 18	1 17 33 50 A Out 17 2 1 9 25	16 32 49 A Out 18 8 24	15 31 48 A Out 19 7 23	14 30 47 A Out 20 6 22	13 29 46 A Out 21 5 21	12 28 45 A Out 22 4 20	11 27 44 A Out 23 3 19	10 26 43 A Out 24 2 18	2 3 17 33 50 A Out 25 2 3 9 25	16 32 49 A Out 26 8 24	31 48 A Out 27 7 23	30 47 A Out 28 6 22	13 29 46 A Out 29 5 21	12 28 45 A Out 30 4 20	11 27 44 A Out 31 3 19	26 43 A Out 32 2 18



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2 QDC Cards		P		D In 1 - 2	D In 3 - 4	D In 5 - 6	D In 7 - 8	D In 9 - 10	D In 11 - 12	D In 13 - 14	D In 15 - 16	D In 17 - 18	D In 19 - 20	D In 21 - 22	D In 23 - 24	D In 25 - 26	D In 27 - 28	D In 29 - 30	D In 31 - 32	D In 33 - 34	D In 35 - 36	D In 37 - 38	D In 39 - 40	D In 41 - 42	D In 43 - 44	D In 45 - 46	D In 47 - 48
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2 QDC Cards	Digital Outputs	Port QDC Con Pins	+ GND -		D Out 3 - 4 12 28 45	D Out 5 - 6 11 27 44	D Out 7 - 8 10 26 43		D Out 11 - 12 4 20 37	D Out 13 - 14 3 19 36	D Out 15 - 16 2 18 35	D Out 17 - 18 2 2 13 29 46	D Out 19 - 20 12 28 45	11 27	D Out 23 - 24 10 26 43	D Out 25 - 26 2 2 5 21 38	4 20	D Out 29 - 30 3 19 36	D Out 31 - 32 2 18 35	D Out 33 - 34 2 3 5 29 46	D Out 35 - 36 45	Out 37 - 38 3 27 44			D Out 43 - 44 4 20 37	D Out 45 - 46 3 19 36	D Out 47 - 48 2 18 35
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nputs	puts	Con Pins	+ GND	2 17 33	16 32	15 31	14 30	2 9 25	8 8	7 23	6 22	2 17 33	16 32	15 31	14 30	2 9 25	8 24	7 23	6 22	3 9 2	8	7 2	6 2	1 9 2	8	7 2	6 22
16 Analog Inputs	Digital Inputs	0 DC		-				-				7				7				2				7			
16 Ar		Port		D In 1 - 2	D In 3 - 4	D In 5 - 6	D In 7 - 8	D In 9 - 10	D In 11 - 12	D In 13 - 14	D In 15 - 16	D In 17 - 18	D In 19 - 20	D In 21 - 22	D In 23 - 24	D In 25 - 26	D In 27 - 28	D In 29 - 30	D In 31 - 32	D In 33 - 34	D In 35 - 36	D In 37 - 38	D In 39 - 40	D In 41 - 42	D In 43 - 44	D In 45 - 46	D In 47 - 48
48 Digital I/Os	outs	Pins	+ GND -																								
8 Digi	Analog Outputs	Con																									
4	Analo	QDC																									
Q		Port																									
48 Track Mixed I/O	its	Pins	+ GND -			15 31 48	14 30 47	29			10 26 43		16 32 49		14 30 47	13 29 46		27	10 26 43								
48 Tracl	Analog Inputs	QDC Con		-								1 3															
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Analog Ir	nalog Outpı	QDC Con	+	1 1 9	~							1 3								1 2								-			
32 Analog Inputs	Analog Outputs	adc	+	Out 1 1 1 9	Out 2 E	Out 3	Out 4	Out 5	Out 6	Out 7	Out 8	-	Dut 10	Dut 11	Jut 12	out 13	Dut 14	Jut 15	but 16	-	Dut 18	but 19	Dut 20	but 21	Dut 22	Dut 23	0ut 24	-	Jut 26	)ut 27	0ut 28
	Analog Outpu		+	A Out 1 1 1 9	A Out 2 E	A Out 3	A Out 4	A Out 5	A Out 6	A Out 7	A Out 8	A Out 9 1 3	A Out 10	A Out 11	A Out 12	A Out 13	A Out 14	A Out 15	A Out 16	-	A Out 18	A Out 19	A Out 20	A Out 21	A Out 22	A Out 23	A Out 24	A Out 25 1 2	A Out 26	A Out 27	A Out 28
	Analog Outpr	Port QDC	+	50	49				_	44	43	50 A Out 9 1	49		47	46		44	43	50 A Out 17 1	49		47 A	46 A	<	44	43	A Out 25 1			47 A Out 28
		adc	GND	33 50	32 49	31 48	30 47	29 46	28 45	27 44	26 43	33 50 A Out 9 1	32 49	31 48	30 47	29 46	28 45	27 44	26 43	33 50 A Out 17 1	32 49	31 48	30 47 A	29 46 A	28 45 A	27 44	26 43	33 50 A Out 25 1	32 49	31 48	30 47
		Pins Port QDC		33 50	49	31 48	30 47	29 46	45	27 44	43	17 33 50 A Out 9 1	32 49	31 48	30 47	29 46	45	27 44	43	33 50 A Out 17 1	32 49	31 48	30 47 A	46 A	28 45 A	27 44	43	17 33 50 A Out 25 1	32 49	31 48	47
		Con Pins Port QDC	GND	33 50	32 49	31 48	30 47	29 46	28 45	27 44	26 43	33 50 A Out 9 1	32 49	31 48	30 47	29 46	28 45	27 44	26 43	33 50 A Out 17 1	32 49	31 48	30 47 A	29 46 A	28 45 A	27 44	26 43	33 50 A Out 25 1	32 49	31 48	30 47
48 I rack Analog 32 Analog Ir		Pins Port QDC	GND	33 50	32 49	31 48	30 47	29 46	28 45	27 44	26 43	17 33 50 A Out 9 1	32 49	31 48	30 47	29 46	28 45	27 44	10 26 43	33 50 A Out 17 1	32 49	31 48	30 47 A	29 46 A	28 45 A	27 44	26 43	17 33 50 A Out 25 1	32 49	31 48	30 47
	Analog Inputs Analog Outpu	Con Pins Port QDC	GND	1 1 1 17 33 50	32 49	3 15 31 48	14 30 47	13 29 46	12 28 45	27 44	26 43	17 33 50 A Out 9 1	32 49	15 31 48	14 30 47	13 29 46	28 45	27 44	10 26 43	2 1 17 33 50 A Out 17 1	32 49	15 31 48	14 30 47 A	29 46 A	12 28 45 A	11 27 44	10 26 43	2 3 17 33 50 A Out 25 1	16 32 49	15 31 48	30 47

48 Track Analog 32 Analog Inputs 48 Analog Outputs 2 QDC Cards

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38	37	36	35	42	41	4	39	38	37	36	35	42	4	4	39	38	37	36	35
7	20	19		52															
5	4	e	2	6	80	7	9	5	4	e	2	3	80	7	9	5	4	e	2
				-								e							
				2								2						_	
6	~	-	~	m	-	10		~	ŝ	G	0	-	2	m	<b>et</b>	10	6		=
A Out 29	A Out 30	A Out 31	A Out 32	A Out 33	A Out 34	A Out 35	A Out 36	A Out 37	A Out 38	A Out 39	A Out 40	A Out 41	A Out 42	A Out 43	A Out 44	A Out 45	A Out 46	A Out 47	A Out 4
	5 A Out 30			A Out 33	A Out 34	A Out 35	A Out 36	A Out 37	A Out 38	A Out 39	A Out 40	A Out 41	A Out 42	A Out 43	A Out 44	A Out 45	A Out 46	A Out 47	A Out 4
46	45	44	43	A Out 33	A Out 34	A Out 35	A Out 36	A Out 37	A Out 38	A Out 39	A Out 40	A Out 41	A Out 42	A Out 43	A Out 44	A Out 45	A Out 46	A Out 47	A Out 4
29 46		27 44	26 43	A Out 33	A Out 34	A Out 35	A Out 36	A Out 37	A Out 38	A Out 39	A Out 40	A Out 41	A Out 42	A Out 43	A Out 44	A Out 45	A Out 46	A Out 47	A Out 4
29 46	28 45	27 44	26 43	A Out 33	A Out 34	A Out 35	A Out 36	A Out 37	A Out 38	A Out 39	A Out 40	A Out 41	A Out 42	A Out 43	A Out 44	A Out 45	A Out 46	A Out 47	A Out 4



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48 Analog Outputs
48 Analog Inputs
48 Digital I/Os
48 Track Analog plus Digital

A	Analog Inpu	g Inp	uts			Ar	Analog Outputs	Out	put	S			Diç	Digital Inputs	nput	s			Dig	Digital Outputs	utpu	its	
Port	QDC	Con		Pins		Port	QDC	Con	_	Pins			Port	QDC	Con	-	Pins		Port	QDC	Con		Pins
			+	GND	,				+	GND	'					+	GND					+	GND
A In 1	-	-	17	33	50	A Out 1	-	-	6	25	42		D In 1 - 2	-	2	17	33	50	D Out 1 - 2	-	7	13	29
A In 2			16	32	49	A Out 2			80	24	41		D In 3 - 4			16	32	49	D Out 3 - 4			12	28
A In 3			15	31	48	A Out 3			7	23	40		D In 5 - 6			15	31	81	D Out 5 - 6			÷	27
A In 4			14	30	47	A Out 4			9	22	39		D In 7 - 8			14	30	47	D Out 7 - 8			₽	26
A In 5			13	29	46	A Out 5			5	2	38	_	D In 9 - 10	-	2	6	25 4	42	D Out 9 - 10	-	2	S	21
A In 6			12	28	45	A Out 6			4	20	37		0 In 11 - 12			œ	24	41	D Out 11 - 12			4	20
A In 7			÷	27	44	A Out 7			e	19	36		D In 13 - 14			2		40	D Out 13 - 14			e	19
A In 8			9	26	43	A Out 8			2	18	35	<u> </u>	0 In 15 - 16			9		69	D Out 15 - 16			2	18
A In 9	-	e	17	33	50	A Out 9	-	e	6	25	42		D In 17 - 18	7	2	17		50	D Out 17 - 18	2	N	13	29
A In 10			16	32	49	A Out 10			80	24	41		D In 19 - 20			16		49	D Out 19 - 20			12	28
A In 11			15	31	48	A Out 11			7	23	40	<u> </u>	D In 21 - 22			15	31	48	D Out 21 - 22			÷	27
A In 12			14		47	A Out 12			9	22	39		D In 23 - 24			14	30	47	D Out 23 - 24			₽	26
A In 13			13		46	A Out 13			2	2	38		) In 25 - 26	7	2	6	25 4	42	D Out 25 - 26	2	N	2	21
A In 14			12	28	45	A Out 14			4	20	37	<u> </u>	D In 27 - 28			8	24	41	D Out 27 - 28			4	20
A In 15			÷	27	44	A Out 15			e	19	36		0 In 29 - 30			~	23	40	D Out 29 - 30			e	19
A In 16			10	26	43	A Out 16			2	18	35	<u> </u>	D In 31 - 32			9		39	D Out 31 - 32			2	18
A In 17	7	-	17	33	50	A Out 17	2	-	6	25	42	<u> </u>	D In 33 - 34	e	2	17		50	D Out 33 - 34	e	2	13	29
A In 18			16	32	49	A Out 18			8	24	41	<u> </u>	) In 35 - 36			16		49	D Out 35 - 36			12	28
A In 19			15	31	48	A Out 19			7	23	40		D In 37 - 38			15	31	48	D Out 37 - 38			÷	27
A In 20			14	30	47	A Out 20			9	22	39	<u> </u>	D In 39 - 40			14	30	47	D Out 39 - 40			₽	26
A In 21			13	29	46	A Out 21			5	2	38		D In 41 - 42	e	2	6	25 4	42	D Out 41 - 42	e	2	S	21
A In 22			12	28	45	A Out 22			4	20	37		D In 43 - 44			œ	24	41	D Out 43 - 44			4	20
A In 23			÷	27	44	A Out 23			e	19	36		D In 45 - 46			2	23	40	D Out 45 - 46			e	19
A In 24			10	26	43	A Out 24			2	18	35	<u> </u>	D In 47 - 48			9	52	39	D Out 47 - 48			2	18
A In 25	7	e	17	33	50	A Out 25	2	e	6	25	42												
A In 26			16	32	49	A Out 26			8	24	41	_											

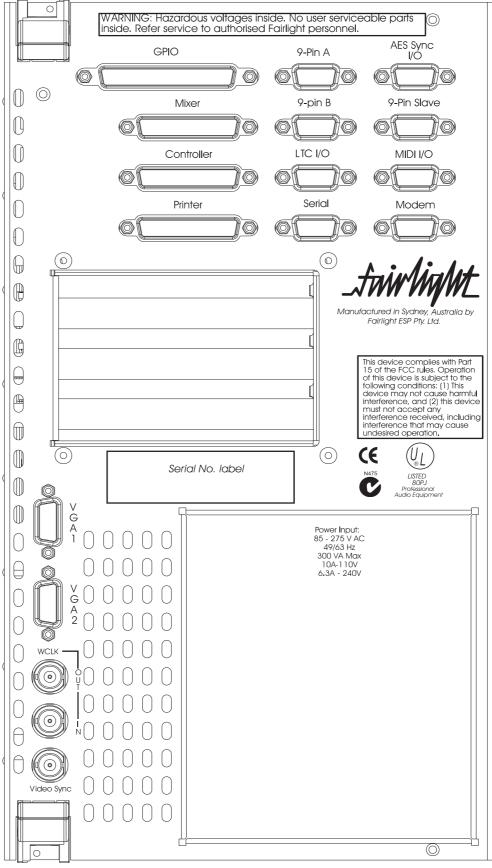
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ß						1 9 25 42					20	19							4 20 37		
A Out 27	A Out 28	A Out 29	A Out 30	A Out 31	A Out 32	A Out 33 3	A Out 34	A Out 35	A Out 36	A Out 37	A Out 38	A Out 39	A Out 40	A Out 41 3	A Out 42	A Out 43	A Out 44	A Out 45	A Out 46	A Out 47	A Out 48
31	14 30 47	29			26	-	32	31	30	29	28	11 27 44	26	3 17 33	32	3	30		12 28 45		10 26 43
A In 27	A In 28	A In 29	A In 30	A In 31	A In 32	A In 33 3	A In 34	A In 35	A In 36	A In 37	A In 38	A In 39	A In 40	A In 41 3	A In 42	A In 43	A In 44	A In 45	A In 46	A In 47	A In 48

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# APPENDIX - CONNECTION & SIGNAL SPECIFICATIONS

#### A2 CONTROL & REFERENCE PINOUTS



#### Engine System I/O Panel



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GPIO

## A3 GENERAL PURPOSE INTERFACE

Input TTL (1 LS Load)

6

Output/Bank

37 Pin D Male

GND
GPIO 6
GPIO 5
GND
GPIOBO2
GPIOBO1
GND
GPIOIN6
GPIOIN 5
GND
GPIOIN2
GPIOIN1
GND
GPIOOUT6
GPIOOUT5
GND
GPIOOUT2
GPIOOUT1
GND
G P I 0 B 0 7
GND
GPI
GPIOBO3
GND
GPIOBO0
GPIOIN7
GND
G P I O IN 4
GPIOIN 3
GND
G P I O IN O
GPIOOUT7
GND
GPIOOUT4



GPIOOUT3

GPIOOUT0

GND



## A4 MIXER

Connector

25 pin D Female

	Mixer	
Ó		١

1       GND         2       RxFFN         3       RxCLKN         4       RxDATN         5       RxWRN         6       NC         7       NC         8       TxFFN         9       TxCLKN         10       TxDATN         11       TxWRPN         12       HSSLDETn         13       SYNCN         14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXACLKP         22       TxDATP         23       TxWRP		
3         RxCLKN           4         RxDATN           5         RxWRN           6         NC           7         NC           8         TxFFN           9         TxCLKN           10         TxDATN           11         TxWRPN           12         HSSLDETn           13         SYNCN           14         RxFFP           15         RxCLKP           16         RxDATP           17         RxWRP           18         NC           19         GND           20         TxFFP           21         TXxCLKP           22         TxDATP           23         TxWRP	1	GND
4         RxDATN           5         RxWRN           6         NC           7         NC           8         TxFFN           9         TxCLKN           10         TxDATN           11         TxWRPN           12         HSSLDETn           13         SYNCN           14         RxFFP           15         RxCLKP           16         RxDATP           17         RxWRP           18         NC           19         GND           20         TxFFP           21         TXxCLKP           22         TxDATP           23         TxWRP	2	RxFFN
5       RxWRN         6       NC         7       NC         8       TxFFN         9       TxCLKN         10       TxDATN         11       TxWRPN         12       HSSLDETn         13       SYNCN         14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	3	RxCLKN
6         NC           7         NC           8         TxFFN           9         TxCLKN           10         TxDATN           11         TxWRPN           12         HSSLDETn           13         SYNCN           14         RxFFP           15         RxCLKP           16         RxDATP           17         RxWRP           18         NC           19         GND           20         TxFFP           21         TXxCLKP           22         TxDATP           23         TxWRP	4	RxDATN
7         NC           8         TxFFN           9         TxCLKN           10         TxDATN           11         TxWRPN           12         HSSLDETn           13         SYNCN           14         RxFFP           15         RxCLKP           16         RxDATP           17         RxWRP           18         NC           19         GND           20         TxFFP           21         TXxCLKP           23         TxWRP	5	RxWRN
8         TxFFN           9         TxCLKN           10         TxDATN           11         TxWRPN           12         HSSLDETn           13         SYNCN           14         RxFFP           15         RxCLKP           16         RxDATP           17         RxWRP           18         NC           19         GND           20         TxFFP           21         TXxCLKP           23         TxWRP	6	NC
9       TxCLKN         10       TxDATN         11       TxWRPN         12       HSSLDETn         13       SYNCN         14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         23       TxWRP	7	NC
10       TxDATN         11       TxWRPN         12       HSSLDETn         13       SYNCN         14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	8	TxFFN
11       TxWRPN         12       HSSLDETn         13       SYNCN         14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	9	TxCLKN
12       HSSLDETn         13       SYNCN         14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXACLKP         22       TxDATP         23       TxWRP	10	TxDATN
13       SYNCN         14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	11	TxWRPN
14       RxFFP         15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	12	HSSLDETn
15       RxCLKP         16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	13	SYNCN
16       RxDATP         17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	14	RxFFP
17       RxWRP         18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	15	RxCLKP
18       NC         19       GND         20       TxFFP         21       TXxCLKP         22       TxDATP         23       TxWRP	16	RxDATP
19     GND       20     TxFFP       21     TXxCLKP       22     TxDATP       23     TxWRP	17	RxWRP
20TxFFP21TXxCLKP22TxDATP23TxWRP	18	NC
21TXxCLKP22TxDATP23TxWRP	19	GND
22TxDATP23TxWRP	20	TxFFP
23 TxWRP	21	TXxCLKP
	22	TxDATP
	23	TxWRP
24 GND	24	GND
25 SYNCP	25	SYNCP

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#### A5 CONTROLLER

Connector

25 pin D Female at Engine End

Controller 37 pin D Male at Console End  $\bigcirc$ 

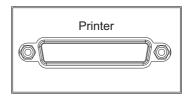
9 pin pigtail D Male at Console End

25 Pin D Type Connector Female	Signal Name	Pairing Details	Signal Name	37 Pin D Type Connector Male
1	GND	Pair 1	GND	1
2	CONTx	Pair 1	RSI2	7
3	CONRx	Pair 2	RSO2	24
5	H-TxDATA	Pair 5	RSO1	6
7	GND	Pair 2	GND	10
9	GND	Pair 5	GND	33
10	CONTxPOS	Pair 3	FS+	13
11	CONRxPOS	Pair 4	TS+	12
20	H-RxDATA	Pair 6	RSI1	25
21	GND	Pair 6	*PR	23
23	CONTxNEG	Pair 3	FS-	31
24	CONRxNEG	Pair 4	TS-	30
9 Pin D Type Connector Male	Signal Name	Pairing Details	Signal Name	37 Pin D Type Connector Male
2	MRXD	Any	MRXD	36
3	MTXD	Any	MTXD	37
5	Gnd	any	Gnd	33

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### A6 PRINTER

Standard 25 pin D female



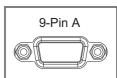
PIN 1	PSTROBEn
PIN 2	PDR0
PIN 3	PDR1
PIN 4	PDR2
PIN 5	PDR3
PIN 6	PDR4
PIN 7	PDR5
PIN 8	PDR6
PIN 9	PDR7
PIN 10	PRACKn
PIN 11	PRBUSYn
PIN 12	PRERROR
PIN 13	PRSELECT
PIN 14	PRAUTOFDn
PIN 15	PRFAULTn
PIN 16	PRINTITn
PIN 17	PRSELINn
PIN 18	GND
PIN 19	GND
PIN 20	GND
PIN 21	GND
PIN 22	GND
PIN 23	GND
PIN 24	GND
PIN 25	GND



### **A8 9-PIN A**

#### Connector

9 pin D Female

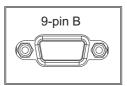


PIN 1	GND
PIN 2	9ARxA
PIN 3	9ATxB
PIN 4	GND
PIN 5	NC
PIN 6	GND
PIN 7	9ARxB
PIN 8	9ATxA
PIN 9	NC

#### A9 9-PIN B

Connector

9 pin D Female



PIN 1	GND
PIN 2	9BRxA
PIN 3	9BTxB
PIN 4	GND
PIN 5	NC
PIN 6	GND
PIN 7	9BRxB
PIN 8	9BTxA
PIN 9	NC

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#### A10 9-PIN SLAVE

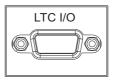
Connector

9 pin D Female



PIN 1 GND PIN 2 9BTxA PIN 3 9RRxB PIN 4 GND PIN 5 NC PIN 6 GND PIN 7 9BTxB PIN 8 9BRxA PIN 9 GND

#### A11 LTC - I/O



Connector 9 pin D Male

Input Level -20dbm to +10dbm

Output Level 0dbm

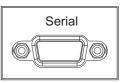
Important note: Unbalanced loads connected to LTC OUT should NOT ground the LTC OUT NEGATIVE signal.

PIN 1	GND
PIN 2	LTC A NEGATIVE
PIN 3	LTC OUT NEGATIVE
PIN 4	GND
PIN 5	LTC B POSITIVE
PIN 6	LTC A POSITIVE
PIN 7	GND
PIN 8	LTC OUT POSITIVE
PIN 9	LTC B NEGATIVE



### A12 SERIAL PORT

Standard 9 pin D male



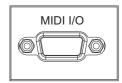
PIN 1	SER_DCD
PIN 2	SER_Rx
PIN 3	SER_Tx
PIN 4	SER_DTR
PIN 5	GND
PIN 6	SER_DSR
PIN 7	SER_RTS
PIN 8	SER_CTS
PIN 9	NC

### A13 MIDI I/O

Connector

9 pin D female

PIN 1	NOT CONNECTED		
PIN 2	MIDI IN NEGATIVE	MIDI IN	DIN PIN 5
PIN 3	MIDI OUT NEGATIVE	MIDI OUT	DIN PIN 5
PIN 4	GND	MIDI THROUGH	DIN PIN 2
PIN 5	MIDI THROUGH POSITIVE	MIDI THROUGH	DIN PIN 4
PIN 6	MIDI POSITIVE	MIDI IN	DIN PIN 4
PIN 7	GND	MIDI OUT	DIN PIN 2
PIN 8	MIDI OUT POSITIVE	MIDI OUT	DIN PIN 4
PIN 9	MIDI THROUGH NEGATIVE	MIDI THROUGH	DIN PIN 5

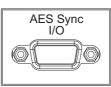


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### A14 AES SYNC I/O

Connector

9 pin D Male

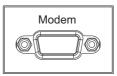


	•
PIN 1	GND
PIN 2	AES IN NEGATIVE
PIN 3	AES OUT POSITIVE
PIN 4	BIPHASE TACH
PIN 5	BIP DIRECTION
PIN 6	AES IN POSITIVE
PIN 7	GND
PIN 8	AES OUT NEGATIVE
PIN 9	NOT CONNECTED

#### A15 MODEM

#### Connector

9 pin D Male



PIN 1	MOD_DCD
PIN 2	MOD_RX
PIN 3	MOD_TX
PIN 4	MOD_DTR
PIN 5	GND
PIN 6	MOD_DSR
PIN 7	MOD_RTS
PIN 8	MOS_CTS
PIN 9	NC

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### A16 VGA

Connector	15 pin High Density D Female
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Resolution 1024(H) x 768(V)

Frequency

HSYNC: 60khz VSYNC: 75hz SVGA STANDARD

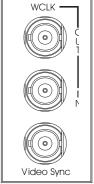
PIN 1	RED
PIN 2	GREEN
PIN 3	BLUE
PIN 4	GND
PIN 5	GND
PIN 6	GND
PIN 7	GND
PIN 8	GND
PIN 10	GND
PIN 11	GND
PIN 12	NC
PIN 13	HSYNC
PIN 14	VSYNC
PIN 15	NC

#### A17 VIDEO SYNC - IN

Connector	BNC	
Input Level	1V p-p 75 Ohms Terminated Internally	WC

#### A18 WORD CLOCK - IN

Connector	BNC
Input	Optically Isolated
Output Impedence	75 Ohms



### A19 WORD CLOCK - OUT

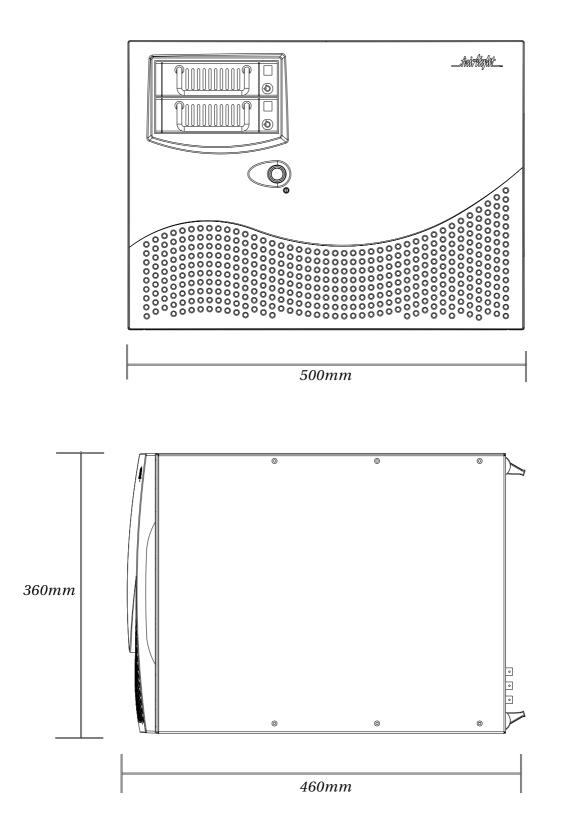
Connector	BNC
Output Level	>4.3V TTL
Output Impendence	75 Ohms



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#### A20 DIMENSIONS

#### A20.1 ENGINE





#### A20.2 MFX3.48 CONSOLE

Hight	80 mm
Width	490mm
Depth	345mm

# A20.1 Power Requirments

Input	85-275 Vac
	49-63 Hz 300VA
Fuse	10A - 110V
	6.3A - 240V



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Notes :